

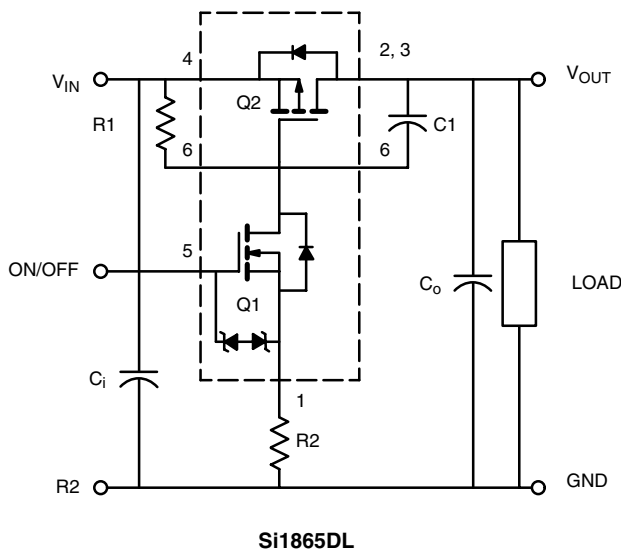
## Load Switch with Level-Shift

| PRODUCT SUMMARY      |                                  |                    |
|----------------------|----------------------------------|--------------------|
| V <sub>DS2</sub> (V) | R <sub>DS(on)</sub> (Ω)          | I <sub>D</sub> (A) |
| 1.8 to 8             | 0.215 at V <sub>IN</sub> = 4.5 V | ± 1.2              |
|                      | 0.300 at V <sub>IN</sub> = 2.5 V | ± 1.0              |
|                      | 0.440 at V <sub>IN</sub> = 1.8 V | ± 0.7              |

### DESCRIPTION

The Si1865DL includes a p- and p-channel MOSFET in a single SC70-6 package. The low on-resistance p-channel TrenchFET is tailored for use as a load switch. The n-channel, with an external resistor, can be used as a level-shift to drive the p-channel load-switch. The n-channel MOSFET has internal ESD protection and can be driven by logic signals as low as 1.5 V. The Si1865DL operates on supply lines from 1.8 V to 8 V, and can drive loads up to 1.2 A.

### APPLICATION CIRCUITS

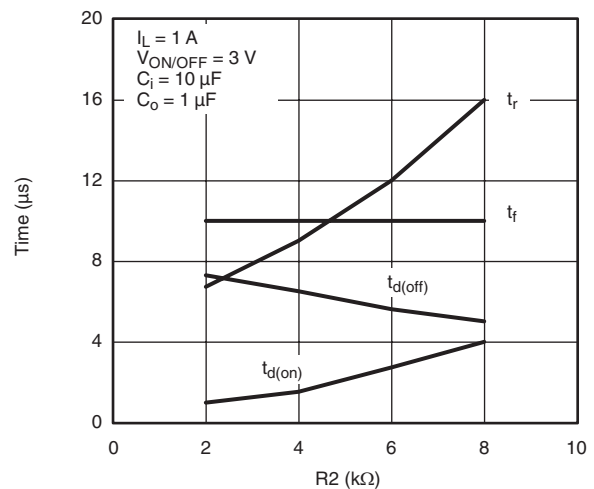


| COMPONENTS |                            |                         |
|------------|----------------------------|-------------------------|
| R1         | Pull-Up Resistor           | Typical 10 kΩ to 1 mΩ*  |
| R2         | Optional Slew-Rate Control | Typical 0 kΩ to 100 kΩ* |
| C1         | Optional Slew-Rate Control | Typical 1000 pF         |

\* Minimum R1 value should be least 10 x R2 to ensure Q1 turn-on.

### FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- 215 mΩ Low R<sub>DS(on)</sub> TrenchFET<sup>®</sup>
- 1.8 V to 8 V Input
- 1.5 V to 8 V Logic Level Control
- Low Profile, Small Footprint SC70-6 Package
- 2000 V ESD Protection On Input Switch, V<sub>ON/OFF</sub>
- Adjustable Slew-Rate
- 1.8 V Rated
- Compliant to RoHS Directive 2002/95/EC

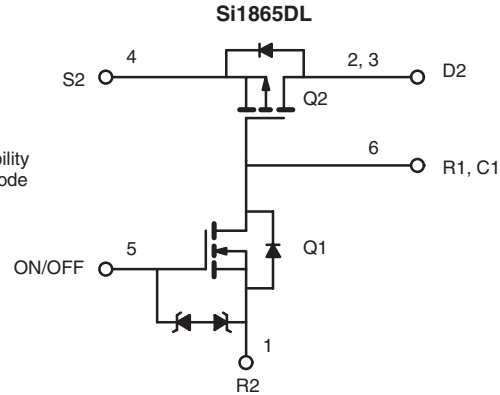
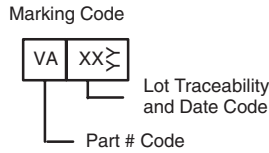
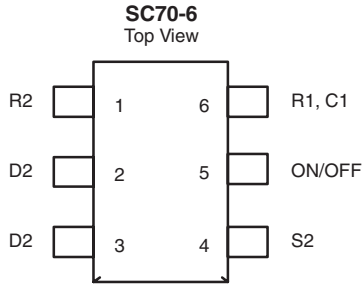


Note: For R2 switching variations with other V<sub>IN</sub>/R1 combinations see Typical Characteristics

**Switching Variation**  
R2 at V<sub>IN</sub> = 2.5 V, R1 = 20 kΩ

The Si1865DL is ideally suited for high-side load switching in portable applications. The integrated n-channel level-shift devices saves space by reducing external components. The slew rate is set externally so that rise-times can be tailored to different load types.

## FUNCTIONAL BLOCK DIAGRAM



Ordering Information: Si1865DL-T1-E3 (Lead (Pb)-free)  
Si1865DL-T1-GE3 (Lead (Pb)-free and Halogen-free)

| ABSOLUTE MAXIMUM RATINGS $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise noted |                            |             |                  |
|--|----------------------------|-------------|------------------|
| Parameter  | Symbol                     | Limit       | Unit             |
| Input Voltage  | $V_{IN}$                   | 8           | V                |
| ON/OFF Voltage   | $V_{ON/OFF}$               | 8           |                  |
| Load Current   | Continuous <sup>a, b</sup> | $\pm 1.2$   | A                |
|  | Pulsed <sup>b, c</sup>     | $\pm 3$     |                  |
| Continuous Intrinsic Diode Conduction <sup>a</sup>                                 | $I_S$                      | - 0.4       |                  |
| Maximum Power Dissipation <sup>a</sup>   | $P_D$                      | 0.4         | W                |
| Operating Junction and Storage Temperature Range                                   | $T_J, T_{stg}$             | - 55 to 150 | $^\circ\text{C}$ |
| ESD Rating, MIL-STD-883C Human Body Model (100 pF, 1500 $\Omega$ )                 | ESD                        | 2           | kV               |

| THERMAL RESISTANCE RATINGS                                    |            |         |         |                    |
|---|------------|---------|---------|--------------------|
| Parameter   | Symbol     | Typical | Maximum | Unit               |
| Maximum Junction-to-Ambient (continuous current) <sup>a</sup> | $R_{thJA}$ | 260     | 320     | $^\circ\text{C/W}$ |
| Maximum Junction-to-Foot (Q2)                                 | $R_{thJC}$ | 180     | 220     |                    |

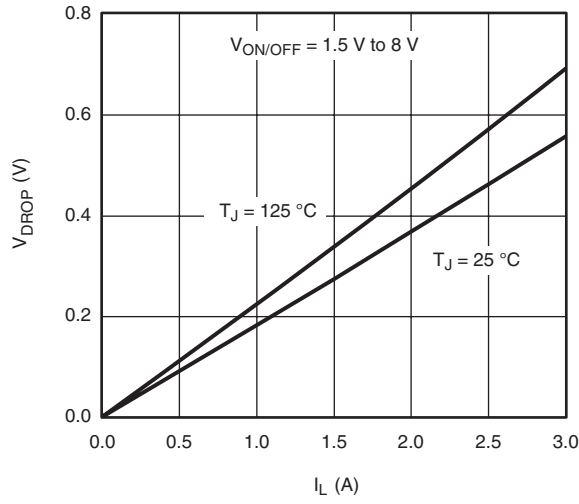
| SPECIFICATIONS $T_J = 25\text{ }^\circ\text{C}$ unless otherwise noted |              |  |      |       |       |               |
|--|--------------|--|------|-------|-------|---------------|
| Parameter  | Symbol       | Test Conditions  | Min. | Typ.  | Max.  | Unit          |
| <b>OFF Characteristics</b>   |              |  |      |       |       |               |
| Reverse Leakage Current  | $I_{R}$      | $V_{IN} = 8\text{ V}, V_{ON/OFF} = 0\text{ V}$                                 |      |       | 1     | $\mu\text{A}$ |
| Diode Forward Voltage  | $V_{D}$      | $I_S = -0.4\text{ A}$  |      | 0.85  | 1.1   | V             |
| <b>ON Characteristics</b>  |              |  |      |       |       |               |
| Input Voltage  | $V_{IN}$     |  | 1.8  |       | 8     | V             |
| On-Resistance (P-Channel) at 1 A                                       | $R_{DS(on)}$ | $V_{ON/OFF} = 1.5\text{ V}, V_{IN} = 4.5\text{ V}, I_D = 1.2\text{ A}$         |      | 0.180 | 0.215 | $\Omega$      |
|  |              | $V_{ON/OFF} = 1.5\text{ V}, V_{IN} = 2.5\text{ V}, I_D = 1.0\text{ A}$         |      | 0.250 | 0.300 |               |
|  |              | $V_{ON/OFF} = 1.5\text{ V}, V_{IN} = 1.8\text{ V}, I_D = 0.7\text{ A}$         |      | 0.367 | 0.440 |               |
| On-State (P-Channel) Drain-Current                                     | $I_{D(on)}$  | $V_{IN-OUT} \leq 0.2\text{ V}, V_{IN} = 5\text{ V}, V_{ON/OFF} = 1.5\text{ A}$ | 1    |       |       | A             |
|  |              | $V_{IN-OUT} \leq 0.3\text{ V}, V_{IN} = 3\text{ V}, V_{ON/OFF} = 1.5\text{ A}$ | 1    |       |       |               |

Notes:

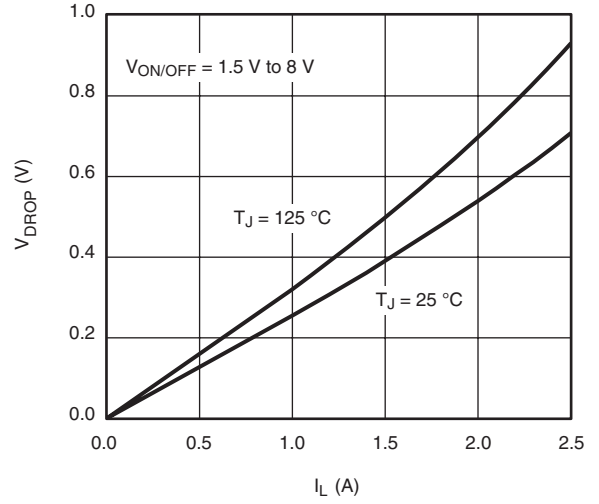
- a) Surface mounted on FR4 board.
- b)  $V_{IN} = 8\text{ V}, V_{ON/OFF} = 8\text{ V}, T_A = 25\text{ }^\circ\text{C}$ .
- c) Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

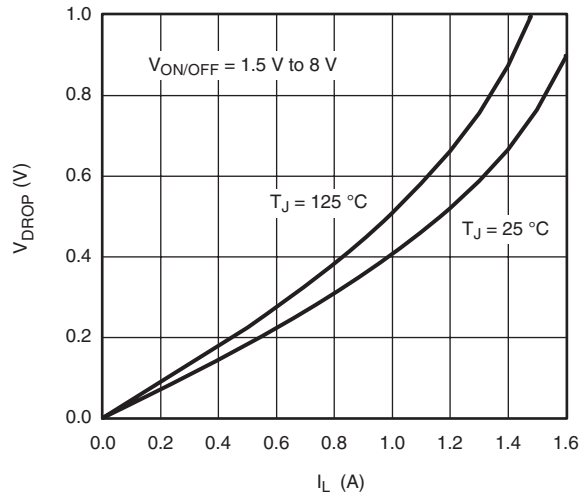
**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



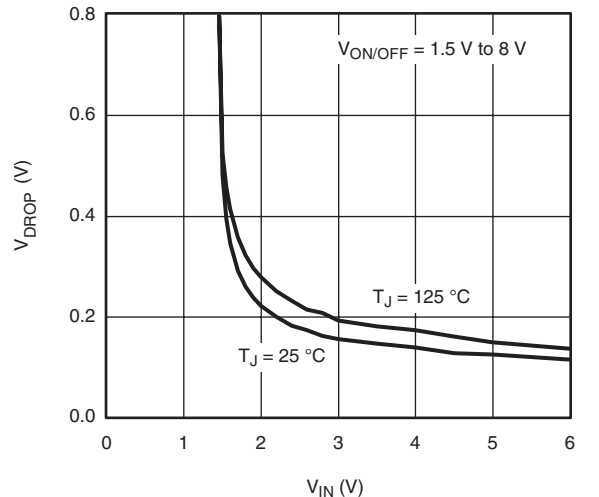
**V<sub>DROP</sub> vs. I<sub>L</sub> at V<sub>IN</sub> = 4.5 V**



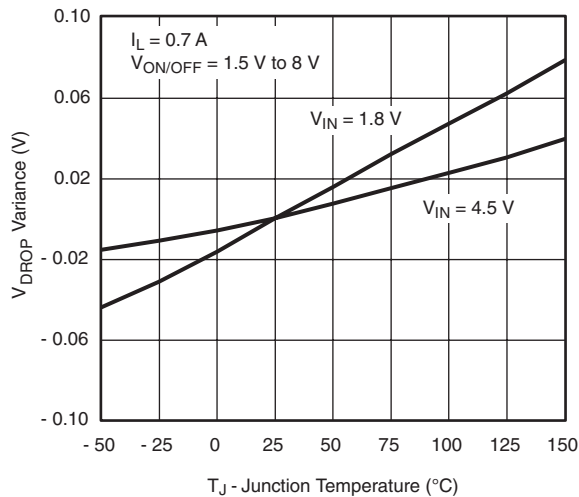
**V<sub>DROP</sub> vs. I<sub>L</sub> at V<sub>IN</sub> = 2.5 V**



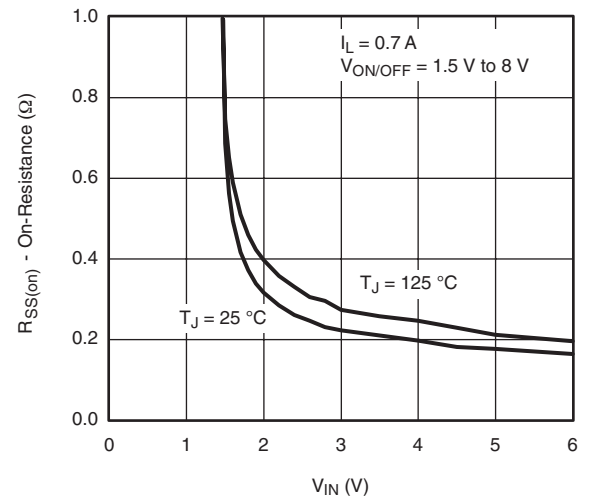
**V<sub>DROP</sub> vs. I<sub>L</sub> at V<sub>IN</sub> = 1.8 V**



**V<sub>DROP</sub> vs. I<sub>L</sub> at I<sub>L</sub> = 0.7 A**

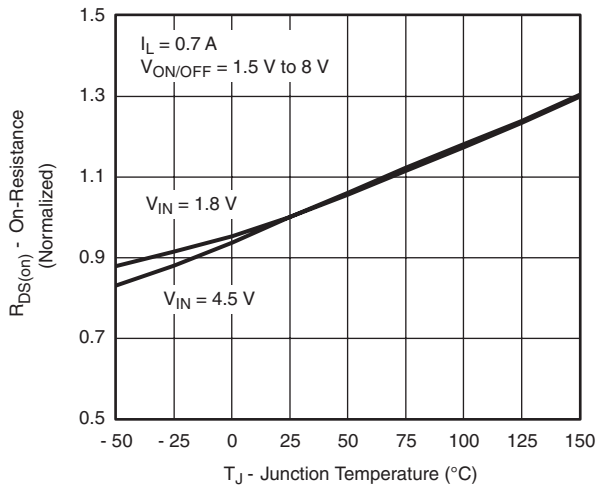


**V<sub>DROP</sub> Variance vs. Junction Temperature**

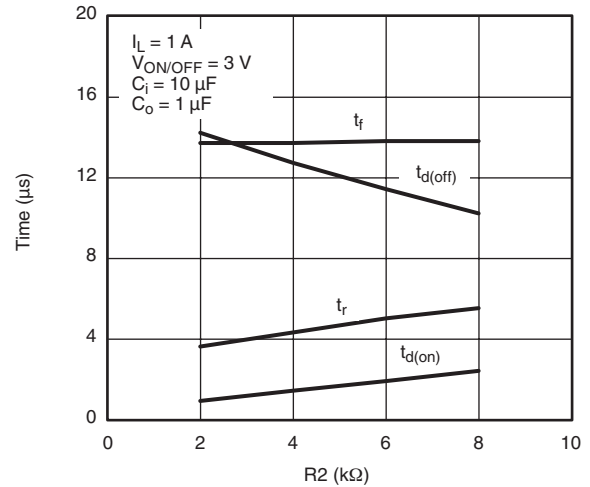


**On-Resistance vs. Input Voltage**

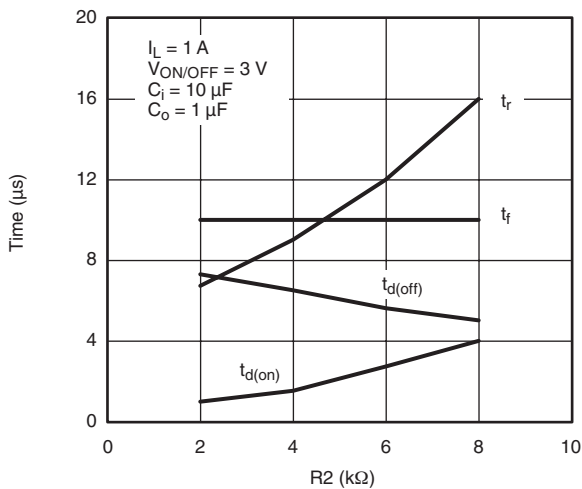
**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



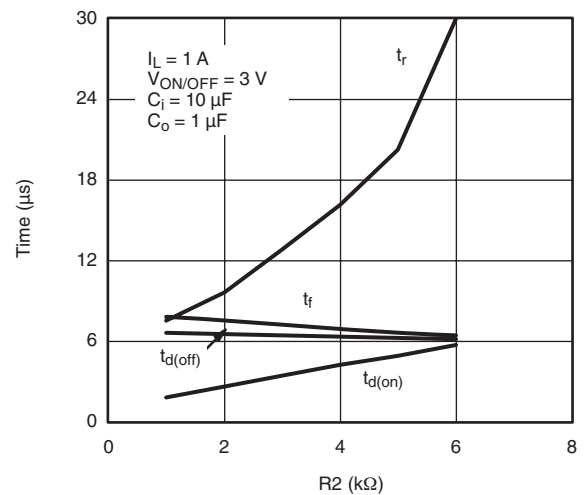
**Normalized On-Resistance vs. Junction Temperature**



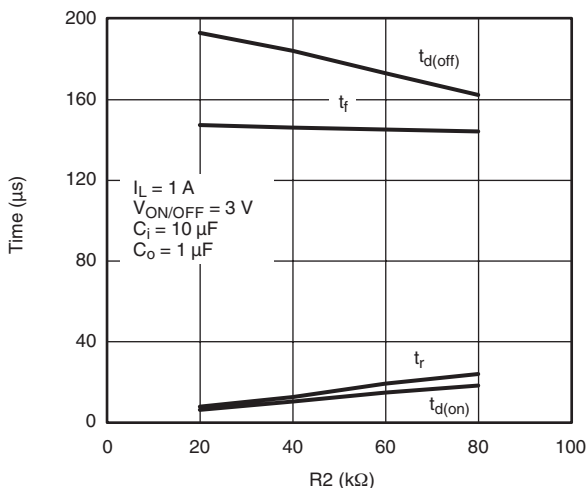
**Switching Variation R2 at V<sub>IN</sub> = 1.8 V, R1 = 20 kΩ**



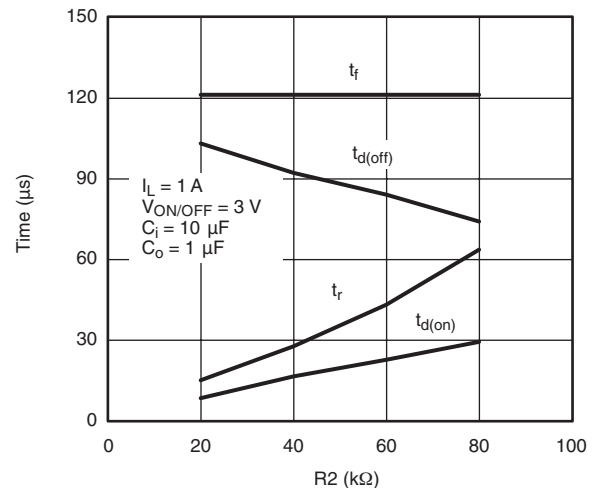
**Switching Variation R2 at V<sub>IN</sub> = 2.5 V, R1 = 20 kΩ**



**Switching Variation R2 at V<sub>IN</sub> = 1.8 V, R1 = 20 kΩ**

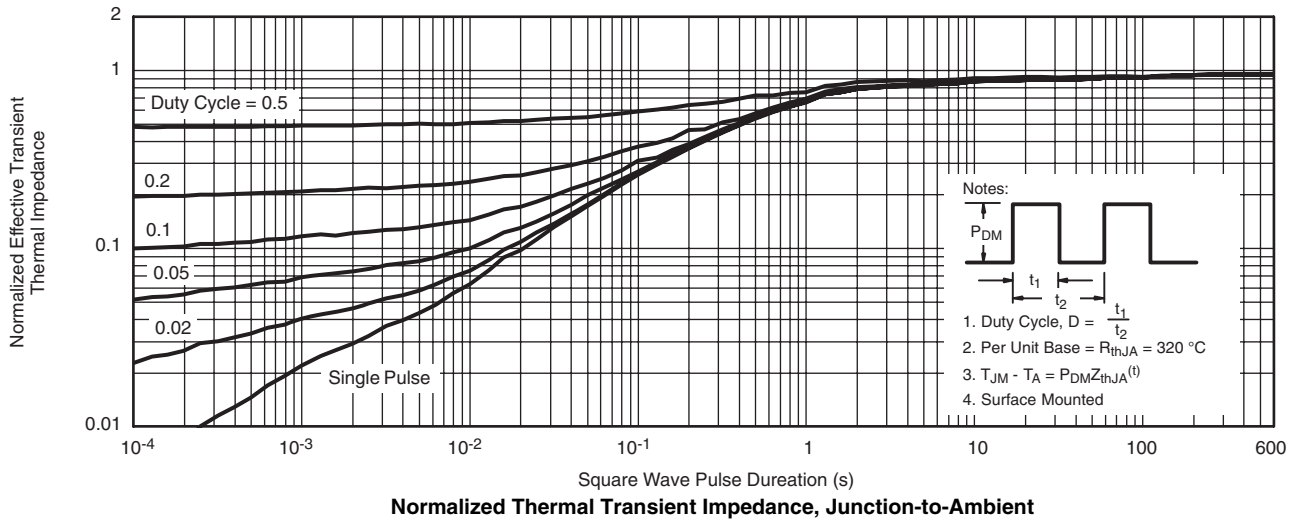
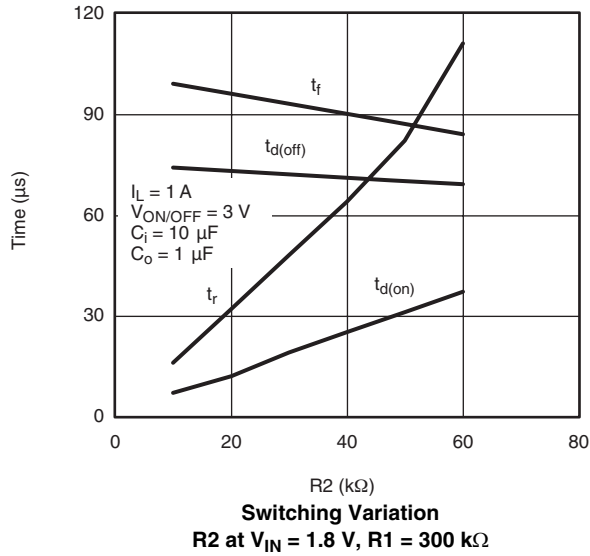


**Switching Variation R2 at V<sub>IN</sub> = 4.5 V, R1 = 300 kΩ**



**Switching Variation R2 at V<sub>IN</sub> = 2.5 V, R1 = 300 kΩ**

**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



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