### Motor driver ICs

# 3-phase motor driver for CD-ROMs BA6849FP / BA6849FP-Y / BA6849FM / BA6849FS

The BA6849 series are ICs developed for CD-ROM spindle motor drives. These ICs possess a short brake and reverserotation brake for two types of brake functions, and also contain FG output and rotation direction detection (FR) circuits, making them high-functionality and high-performance ICs.

### Applications

CD-ROM, CD-R, CD-RW, DVD-ROM, and DVD-RAM

### Features

- 1) Three-phase, full-wave, pseudo-linear drive system.
- 2) Built-in power save and thermal shutdown functions.
- 3) Built-in current limiter and Hall bias circuits.
- 5) Built-in rotation direction detector.
- 6) Built-in reverse rotation prevention circuit.
- 7) Built-in short brake pin.

4)	Built-in	FG	output
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### •Absolute maximum ratings (Ta = $25^{\circ}$ C)

Parameter		Symbol	Limits	Unit
Applied voltage (with 5V power supply)		Vcc	7	v
Applied voltage (motor power supply1)		V <sub>M1</sub>	16	v
Applied voltage (motor power		V <sub>M2</sub>	16	v
Power dissipation	BA6849FM	- Pd	2200*1	mW
	BA6849FP		1700*2	mW
	BA6849FP-Y		1450*3	mW
	BA6849FS		1000*4	mW
Operating temperature		Topr	-20~+75	Ĵ
Storage temperature		Tstg	-55~+150*5	Ĵ
Output current		lout	1300*6	mA

\*1 Reduced by 17.6mW for each increase in Ta of 1°C over 25°C.

\*2 When mounted on a 70mm×70mm×1.6mm glass epoxy board.

Reduced by 13.6mW for each increase in Ta of 1°C over 25°C.

\*3 Reduced by 11.6mW for each increase in Ta of 1°C over 25°C.

\*4 Reduced by 8.0mW for each increase in Ta of 1  $^\circ C$  over 25  $^\circ C.$ 

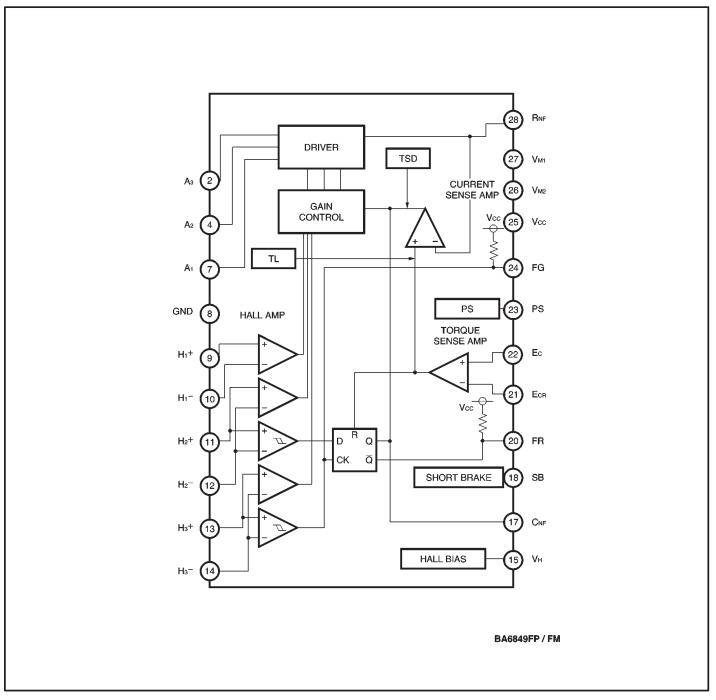
\*5 Tj should not exceed 150°C.

\*6 Should not exceed Pd or ASO values.

•Recommended operating conditions (Ta =  $25^{\circ}$ C)

Parameter	Symbol	Limits	Unit
	Vcc	4.25~5.5	V
Power supply voltage	V <sub>M1</sub>	3.0~15	V
	V <sub>M2</sub>	3.0~15	V

Block diagram



BA6849FP-Y

## Pin descriptionsBA6849FP/BA6849FM

Pin No.	Pin Name	Functiom
2	Аз	Output
4	A2	Output
7	A1	Output
8	GND	GND
9	Hı+	Hall signal input
10	H1 <sup>-</sup>	Hall signal input
11	H <sub>2</sub> +	Hall signal input
12	H2 <sup></sup>	Hall signal input
13	H <sub>3</sub> +	Hall signal input
14	H <sub>3</sub> -	Hall signal input
15	Vн	Hall bias
17	Cnf	For connection of phase compensation capacitor
18	SB	Short brake
20	FR	Rotation direction detection
21	Ecr	Output voltage control reference
22	Ec	Output voltage control
23	PS	Power save
24	FG	FG signal output
25	Vcc	Power supply
26	V <sub>M2</sub>	Motor power supply 2
27	V <sub>M1</sub>	Motor power supply 1
28	RNF	For connection of output current detection resistor
FIN	_	SUB GND

Pin No.	Pin name	Function
1	—	SUB GND
2	Аз	Output
3	A2	Output
5	A1	Output
6	GND	GND
7	H1+	Hall signal input
8	H1 <sup>-</sup>	Hall signal input
9	H <sub>2</sub> +	Hall signal input
10	H2 <sup></sup>	Hall signal input
11	H₃+	Hall signal input
12	H₃ <sup>—</sup>	Hall signal input
13	Vн	Hall bias
15	Cnf	For connection of phase compensation capacitor
16	SB	Short brake
17	FR	Rotation direction detection
18	Ecr	Torque control reference
19	Ec	Torque control
20	PS	Power save
21	FG	FG signal output
22	Vcc	Power supply
23*	V <sub>M2</sub>	Motor power supply 2
23*	<b>V</b> м1	Motor power supply 1
24	RNF	For connection of output current detection resistor

\* Connected within the IC.

Note) Missing pin numbers are N.C.

\* \* Missing pin numbers are N.C.

### MA6849FS

Pin No.	Pin name	Function
1	—	SUB GND
2	Аз	Output
3	A2	Output
5	Aı	Output
6	GND	GND
7	H1+	Hall signal input
8	H1 <sup>-</sup>	Hall signal input
9	H <sub>2</sub> +	Hall signal input
10	H2 <sup></sup>	Hall signal input
11	H₃+	Hall signal input
12	H₃ <sup>—</sup>	Hall signal input
13	Vн	Hall bias
15	Cnf	For connection of phase compensation capacitor
16	SB	Short brake
17	FR	Rotation direction detection
18	Ecr	Torque control reference
19	Ec	Torque control
20	PS	Power save
21	FG	FG signal output
22	Vcc	Power supply
23*	V <sub>M2</sub>	Motor power supply 2
23*	V <sub>M1</sub>	Motor power supply 1
24	RNF	For connection of output current detection resistor

\* Connected within the IC.

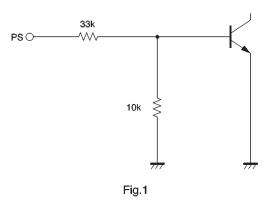
Note) Missing pin numbers are N.C.

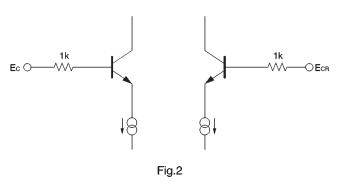


Torque command input

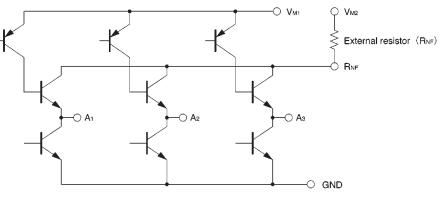
### Input / output circuits

(1) Power save





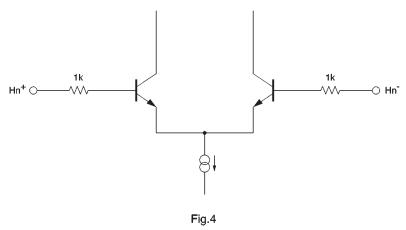
(3) Torque output (A<sub>1</sub>, A<sub>2</sub>, and A<sub>3</sub>)



(2)



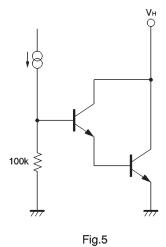
(4) Hall input (H1<sup>+</sup>, H1<sup>-</sup>, H2<sup>+</sup>, H2<sup>-</sup>, H3<sup>+</sup>, H3<sup>-</sup>)

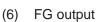


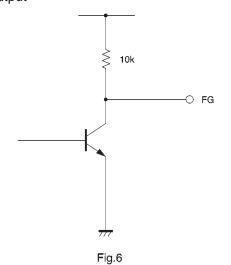
Note: Resistance values are typical values.



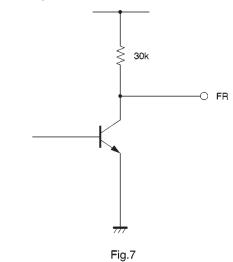
(5) Hall bias



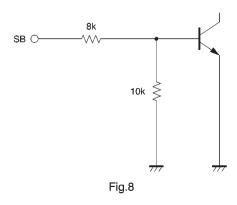




(7) FR Output



(8) Short brake



Note: Resistance values are typical values.

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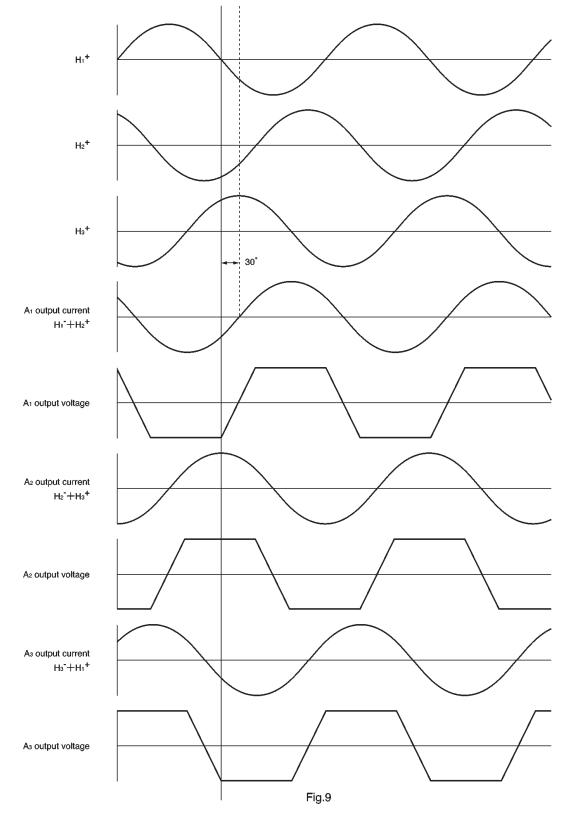
#### Electrical characteristics (unless otherwise noted, Ta = 25°C, Vcc = 5V, Vm1 = 12V, Vm2 = 12V) Parameter Symbol Min. Max. Unit Coniditions Typ. (Total device) Circuit current 1 0 0.2 In the power save ON state ICC1 \_ mΑ 4.1 6.5 Circuit current 2 ICC2 \_ mΑ In the power save OFF state (Power save) ON voltage range VPSON 1.5 V OFF voltage range VPSOFF 3.5 \_ V \_ (Hall bias) Hall bias voltage Vнв ۷ IHB=10mA 0.5 0.9 1.5 (Hall amplifier) Input bias current 0.7 Іна 3.0 μA \_ \_ Same phase input V VHAR 1.5 \_ 4.0 voltage range Minimum input level VINH 50 \_ \_ mVP-P \_ **V**HYS 10 mV H3 hysteresis level 20 40 \_ (Torque command) Input voltage range Ec ٧ 1.0 4.0 \_ "-" offset voltage ECR=2.5V ECOFF--50-20 mV -80"+" offset voltage ECOFF+ 20 50 80 mV ECR=2.5V Input bias current ECIN EC=ECR \_ 0.5 2.0 μA Gec I / O gain 0.61 A/V Ec=1.5V, 2.0V 0.41 0.51 (FG) FG output high level voltage VFGH 4.5 4.8 V $I_{FG} = -20 \,\mu A$ 0 FG output low level voltage VFGL 0.25 0.4 V IFG=3mA DUTY (reference value) 50 \_ % DU (Rotation detection) FR output high level voltage VFRH 4.1 4.4 V IFR=-20 μ A \_ FR output low level voltage ۷ VEBL 0 0.25 0.4 IFR=3mA (Output) Output saturation V 1.5 Vон \_\_\_\_ 1.0 $l_0 = -600 \text{mA}$ high level voltage Output saturation V VOL 0.4 0.8 lo=600mA low level voltage Pre-drive current Ec=0V output open IVML \_ 35 70 mΑ Output limit current TL 560 700 840 mΑ \_ (Short brake) VSBON 3.5 V ON voltage range \_ \_ OFF voltage range ۷ VSBOFF 1.5 \_

ONot designed forradiation resistance.

### Circuit operation

(1) Hall input to coil output

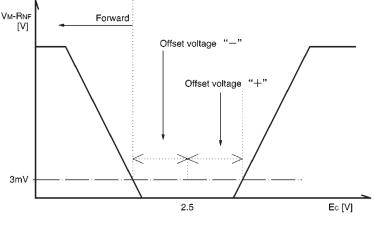
The phase relationship between the Hall input signals and the output current and voltage is shown in Fig.9. The motor position data input via the Hall pins is amplified by the Hall amplifier, and formed into waveforms by the matrix block. These signals are input to the output driver that supplies the drive current to the motor coils.



### (2) Torque command

The  $R_{NF}$  pin voltage with respect to the torque command

(Ec) is as follows:



### Fig.10

	Rotation direction
Ec < Ecr	Forward
Ec > Ecr	Reverse*

<sup>\*</sup> Stops after detecting reverse.

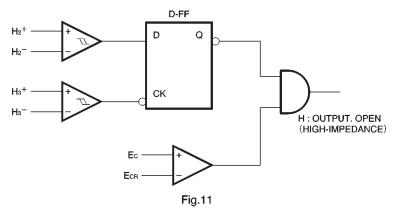
(3) Reverse rotation detection function

The I / O gain ( $G_{EC}$ ) from the E<sub>C</sub> pin to the R<sub>NF</sub> pin (output current) is determined by the R<sub>NF</sub> detector resistor.

 $G_{EC} = 0.255 / R_{NF} [A / V]$ 

The torque limit current  $I_{TL}$  is given by:

 $I_{TL} = 0.35 / R_{NF} [A]$ 



The reverse detection circuit construction is shown in Fig.11.

The phase relationship between the Hall input signals  $H2^+$  and  $H3^+$  becomes as shown in Fig.9, and the reverse rotation detection circuit does not operate.

2) Reverse (Ec > Ecr)

The phase relationship between the signals  $H_2^+$  and  $H_3^+$  is opposite that for forward operation, and the reverse rotation detection circuit operates. The output goes OFF, and becomes open circuit.

	FR signal output pin
Forward	L
Reverse	н



Motor driver ICs

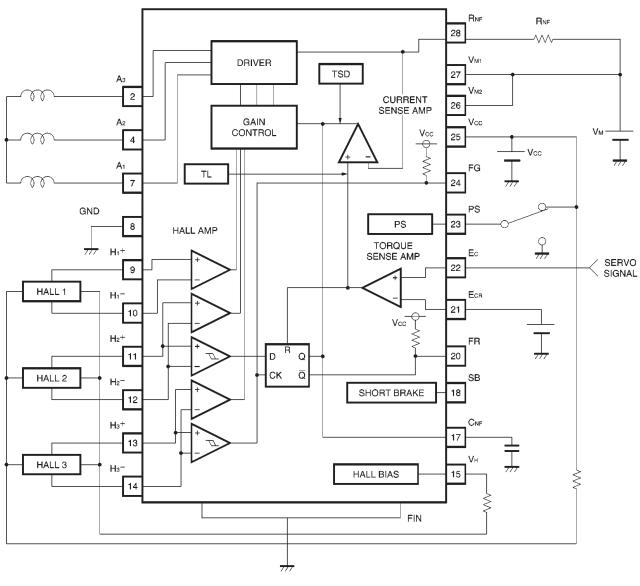
### (4) Short brake

When 3.5V or more is applied to the short brake pin, the upper-side output transistors of all go off, and the lower-side output transistors go on. Short braking operates regardless of the torque command signal.

Application example

### (5) Other circuits

When 3.5V or more is applied to the power save pin, all circuits are on. When 1.5V or less is applied, the IC enters power save mode. Also, the Hall bias pins turn on and off with the power save pin.





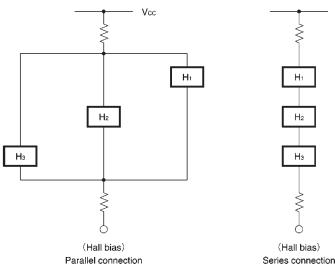
### Operation notes

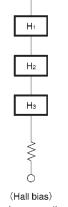
(1) Power save

The power save input is an I/O circuit like the own shown in Fig.1.

The thermal derating characteristics of the power save pin is -8mV / °C, and the resistance will fluctuate between  $\pm 30\%$  so be careful of the input voltage range. (2) Hall input

The input circuit shown in Fig.4 is used for the Hall inputs. The Hall elements can be connected either in series or in parallel.





Vcc

Fig.13

### Electrical characteristics curves

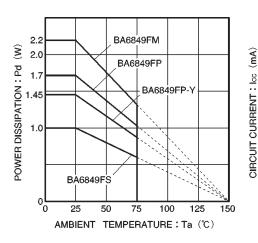
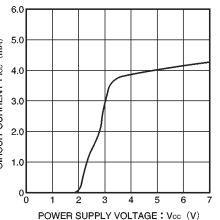
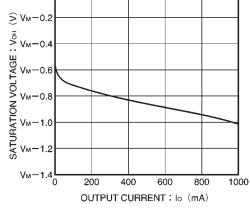


Fig.14 Package derating characteristics





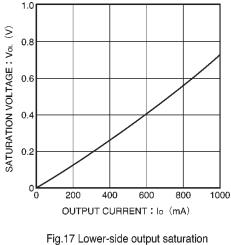
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Fig.15 Power supply current vs. power supply voltage

Fig.16 Upper-side output saturation voltage vs. output current

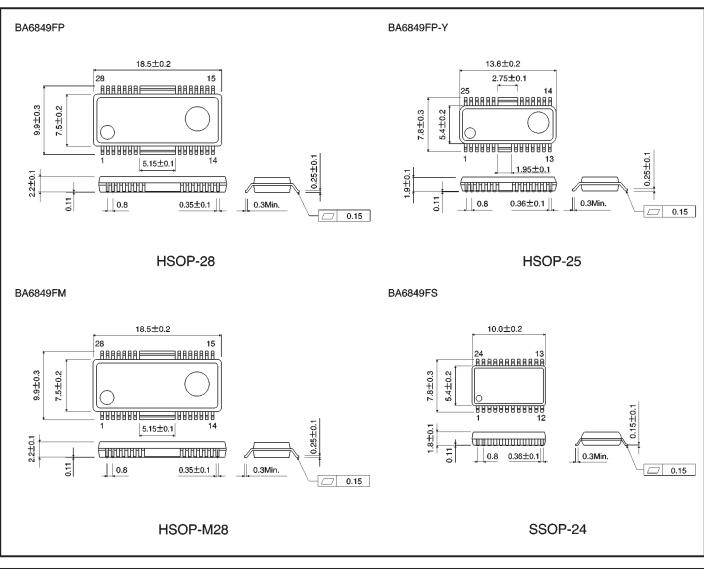
(3) Thermal shutdown (TSD)

When the junction temperature reaches 175°C, the A<sub>1</sub>, A<sub>2</sub>, and A<sub>3</sub> coil outputs go open circuit. The thermal shutdown has approximately 15°C of hysteresis.



voltage vs. output current

External dimensions (Units: mm)



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874