

IPS1021(S)(R)PbF

INTELLIGENT POWER LOW SIDE SWITCH

Features

- Over temperature shutdown
- Over current shutdown
- Active clamp
- Low current & logic level input
- ESD protection
- Optimized Turn On/Off for EMI
- Diagnostic on the input current

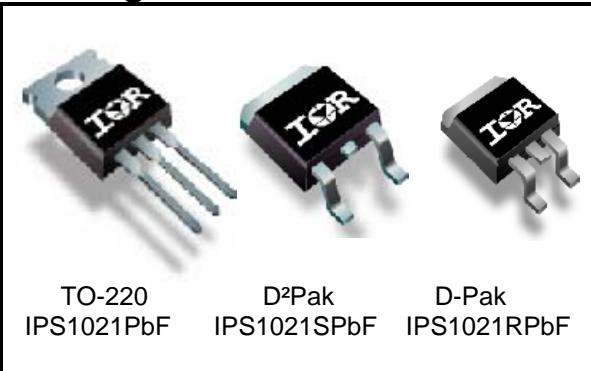
Product Summary

Rds(on)	25mΩ (max.)
Vclamp	36V
Ishutdown	35A (typ.)

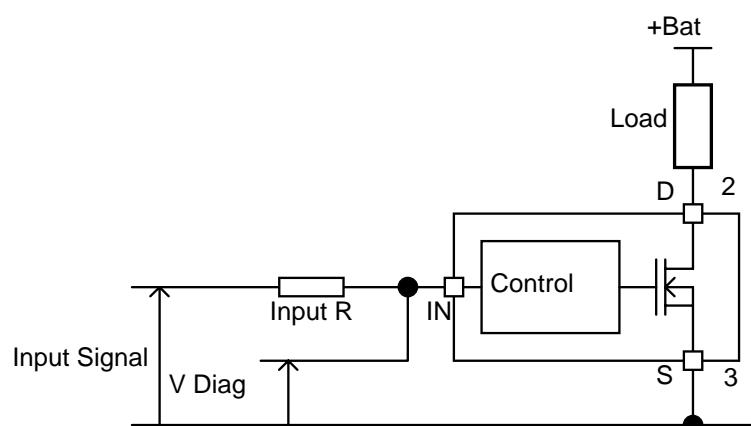
Description

The IPS1021(S)(R)PbF is a three terminal Intelligent Power Switch (IPS) that features a low side MOSFET with over-current, over-temperature, ESD protection and drain to source active clamp. This device offers protections and the high reliability required in harsh environments. The switch provides efficient protection by turning OFF the power MOSFET when the temperature exceeds 165°C or when the drain current reaches 35A. The device restarts once the input is cycled. A serial resistance connected to the input provides the diagnostic. The avalanche capability is significantly enhanced by the active clamp and covers most inductive load demagnetizations.

Packages



Typical Connection



Absolute Maximum Ratings

Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. All voltage parameters are referenced to Ground lead. (T_{ambient}=25°C unless otherwise specified).

Symbol	Parameter	Min.	Max.	Units
V _{ds}	Maximum drain to source voltage	-0.3	36	V
V _{ds} cont.	Maximum continuous drain to source voltage	-	28	V
V _{in}	Maximum input voltage	-0.3	6	V
I _{sd} cont.	Max. diode continuous current (limited by thermal dissipation)	—	4.5	A
P _d	Maximum power dissipation (internally limited by thermal protection)	—	25	W
	R _{th} =5°C/W IPS1021	—	3.1	
	R _{th} =40°C/W IPS1021S 1" sqr. footprint	—	2.5	
ESD	Electrostatic discharge voltage (Human body) C=100pF, R=1500Ω	—	4	kV
	Between drain and source	—	3	
	Other combinations	—	0.5	
	Electrostatic discharge voltage (Machine Model) C=200pF, R=0Ω	—	0.3	
	Between drain and source	—	0.3	
T _j max.	Max. storage & operating temperature junction temperature	-40	150	°C
T _{soldering}	Lead soldering temperature (10 seconds)	—	300	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Units
R _{th1}	Thermal resistance junction to ambient IPS1021 TO-220 free air	50	—	°C/W
R _{th2}	Thermal resistance junction to case IPS1021 TO-220	2.6	—	
R _{th1}	Thermal resistance junction to ambient IPS1021S D ² Pak std. footprint	60	—	
R _{th2}	Thermal resistance junction to ambient IPS1021S D ² Pak 1" sqr. footprint	40	—	
R _{th3}	Thermal resistance junction to case IPS1021S D ² Pak	2.6	—	
R _{th1}	Thermal resistance junction to ambient IPS1021R D-Pak std. footprint	70	—	
R _{th2}	Thermal resistance junction to ambient IPS1021R D-Pak 1" sqr. footprint	50	—	
R _{th3}	Thermal resistance junction to case IPS1021R D-Pak	2.6	—	

Recommended Operating Conditions

These values are given for a quick design. For operation outside these conditions, please consult the application notes.

Symbol	Parameter	Min.	Max.	Units
V _{IH}	High level input voltage	4.5	5.5	
V _{IL}	Low level input voltage	0	0.5	
I _{ds}	Continuous drain current, T _{ambient} =85°C, T _j =125°C, V _{in} =5V	—	13.5	A
	R _{th} =5°C/W IPS1021	—	4.8	
	R _{th} =40°C/W IPS1021S 1" sqr. footprint	—	4.3	
	R _{th} =50°C/W IPS1021R 1" sqr. footprint	—	—	
R _{in}	Recommended resistor in series with IN pin to generate a diagnostic	0.5	10	kΩ
Max L	Max. recommended load inductance (including line inductance) (1)	—	20	μH
Max F	Max. frequency (switching losses = conduction losses)	—	500	Hz
Max t rise	Max. input rising time	—	1	μs

(1) Higher inductance is possible if maximum load current is limited - see figure 11

Static Electrical Characteristics

T_j=25°C, V_{cc}=14V (unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
R _{ds(on)}	ON state resistance T _j =25°C	—	20	25	mΩ	Vin=5V, I _d =8A
	ON state resistance T _j =150°C (2)	—	38	48		
I _{dss1}	Drain to source leakage current	—	0.1	10	μA	V _{cc} =14V, T _j =25°C
I _{dss2}	Drain to source leakage current	—	0.2	20		V _{cc} =28V, T _j =25°C
V clamp1	Drain to source clamp voltage 1	36	39	—	V	I _d =20mA
V clamp2	Drain to source clamp voltage 2	—	40	42		I _d =2A
V _{in} clamp	IN to source pin clamp voltage	5.5	6.5	7.5		I _{in} =1mA
V _{th}	Input threshold voltage	—	1.7	—		I _d =10mA

Switching Electrical Characteristics

V_{cc}=14V, Resistive load=1.5Ω, R_{input}=0Ω, V_{in}=5V, T_j=25°C

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
T _{don}	Turn-on delay time to 20%	10	30	100	μs	See figure 2
T _r	Rise time 20% to 80%	10	30	60		
T _{doff}	Turn-off delay time to 80%	40	150	400		
T _f	Fall time 80% to 20%	15	30	60		
E _{on} + E _{off}	Turn on and off energy	—	2	—	mJ	

Protection Characteristics

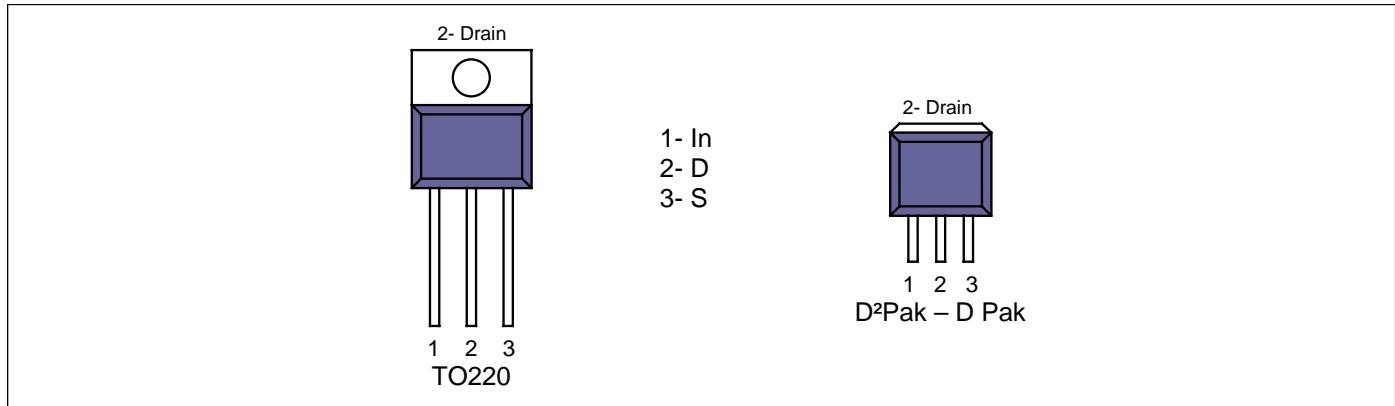
Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
T _{sd}	Over temperature threshold	150(2)	165	—	°C	See figure 1
I _{sd}	Over current threshold	32	45	58	A	See figure 1
O _V	Over voltage protection (not active when the device is ON)	34	37	—	V	
V _{reset}	IN protection reset threshold	—	1.7	—	V	
T _{reset}	Time to reset protection	15(2)	50	200	μs	V _{in} =0V

Diagnostic

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
I _{in, on}	ON state IN positive current	15	32	70	μA	Vin=5V Vin=5V
I _{in, off}	OFF state IN positive current (after protection latched)	150	230	350		

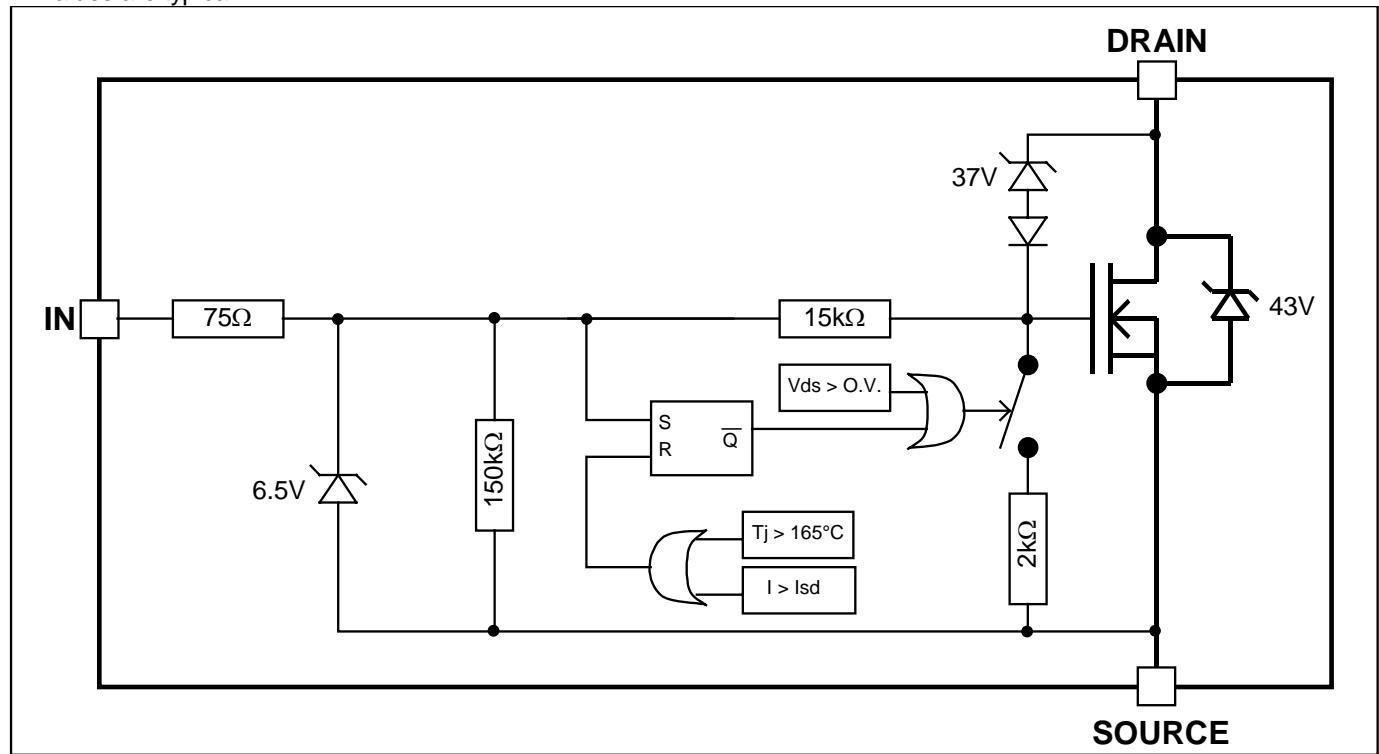
(2) Guaranteed by design

Lead Assignments



Functional Block Diagram

All values are typical



All curves are typical values. Operating in the shaded area is not recommended.

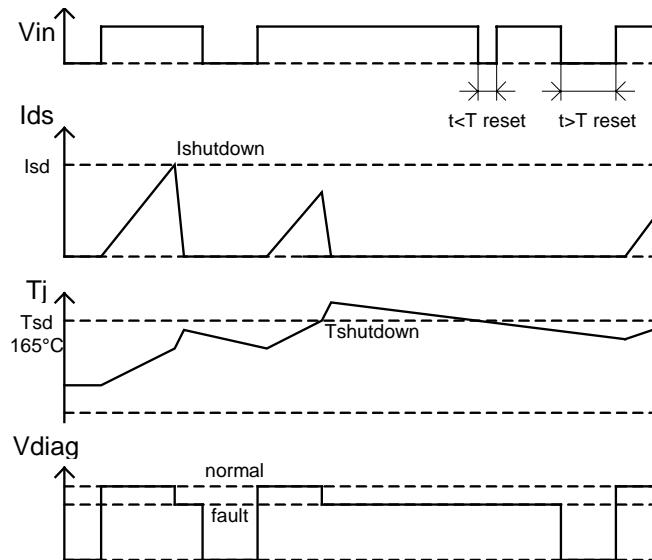


Figure 1 – Timing diagram

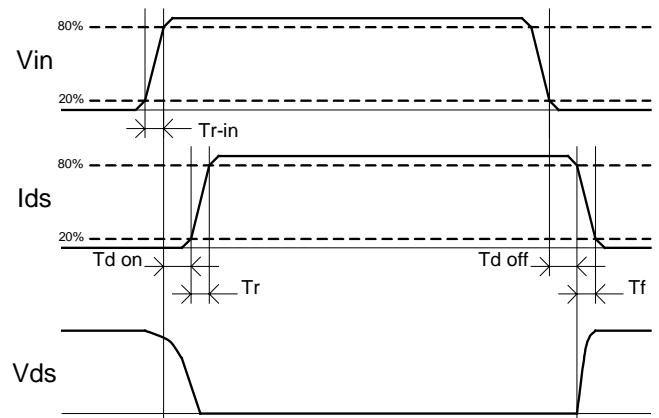


Figure 2 – IN rise time & switching definitions

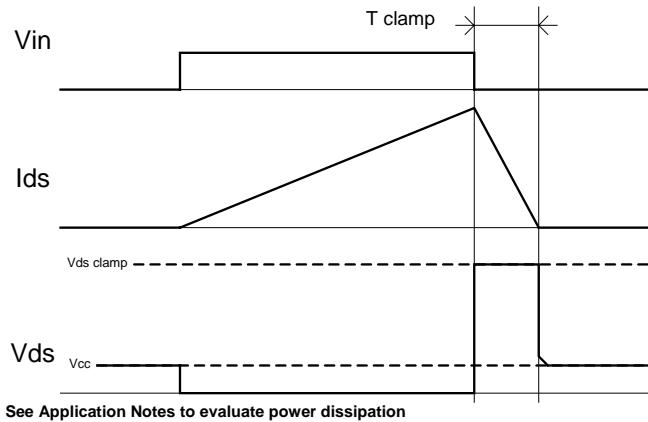


Figure 3 – Active clamp waveforms

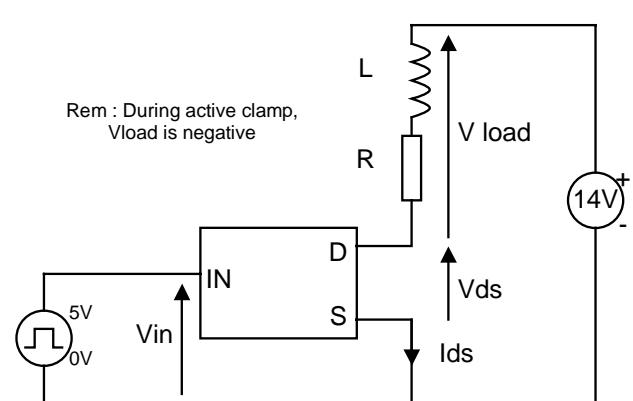


Figure 4 – Active clamp test circuit

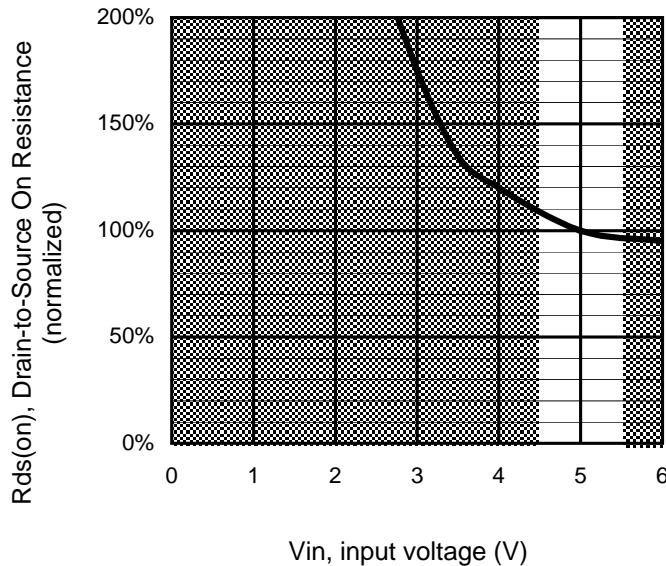


Figure 5 – Normalized R_{ds(on)} (%) Vs Input voltage (V)

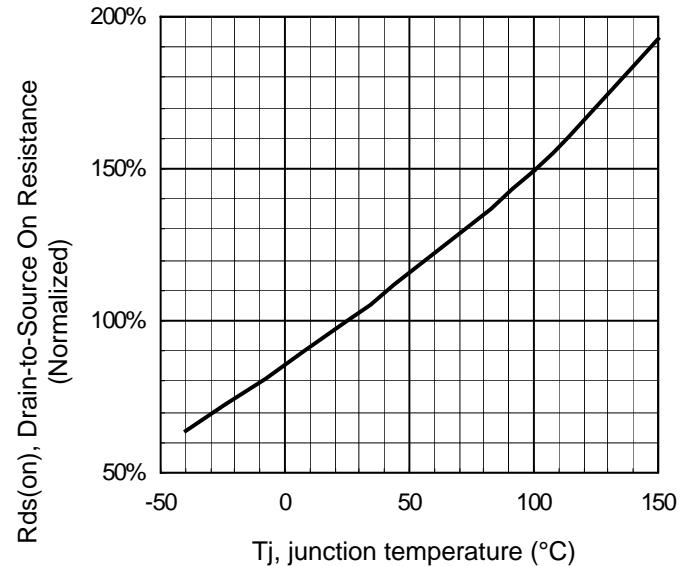


Figure 6 - Normalized R_{ds(on)} (%) Vs T_j (°C)

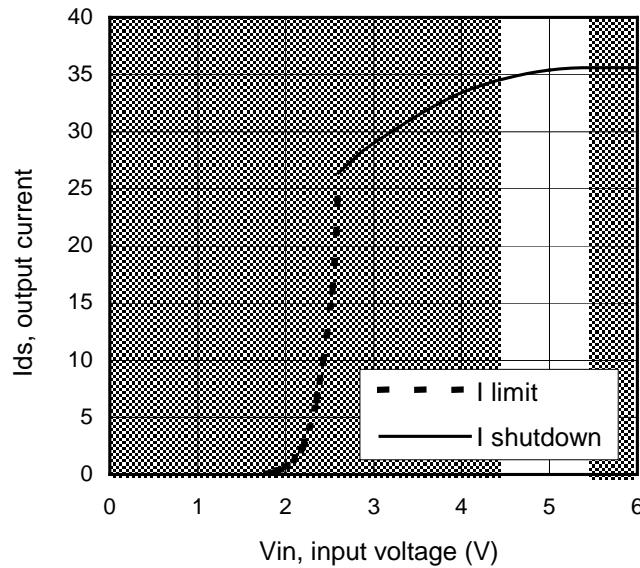


Figure 7 – Current limitation and current shutdown Vs Input voltage (V)

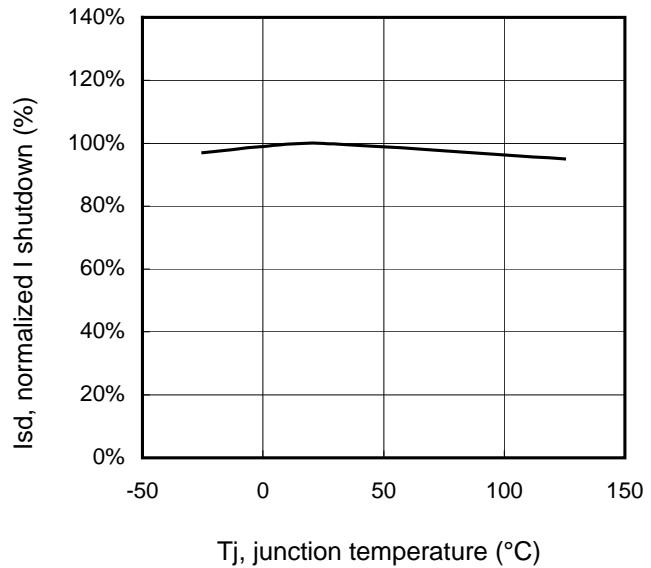


Figure 8 – Normalized I shutdown (%) Vs junction temperature (°C)

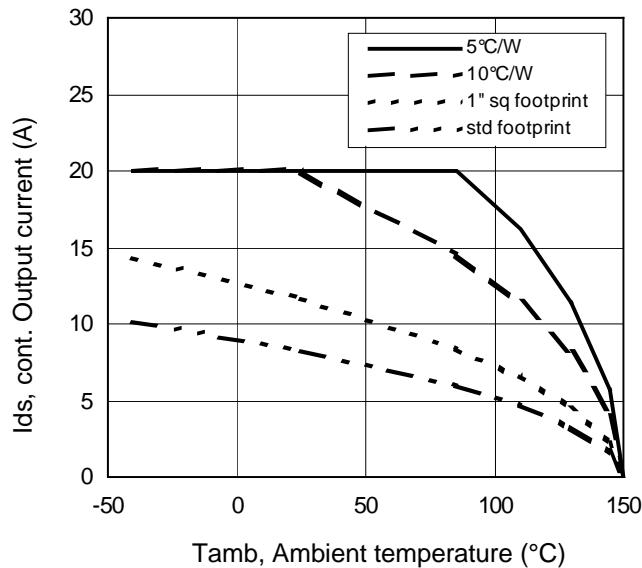


Figure 9 – Max. continuous output current (A) Vs Ambient temperature (°C)

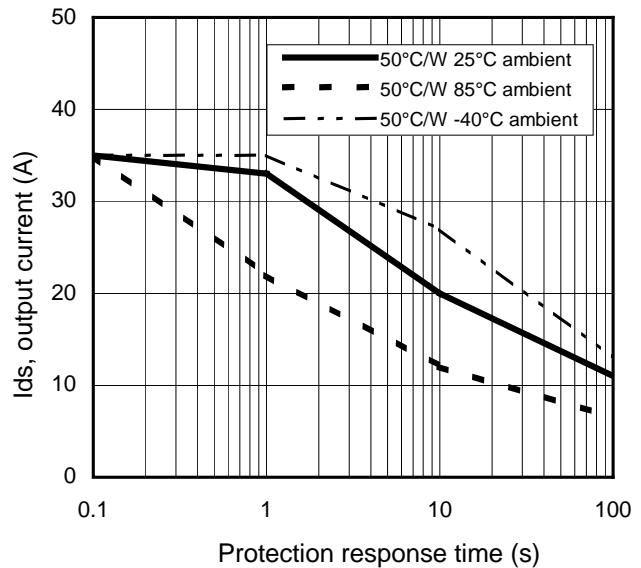


Figure 10 – I_{DS} (A) Vs over temperature protection response time (s)

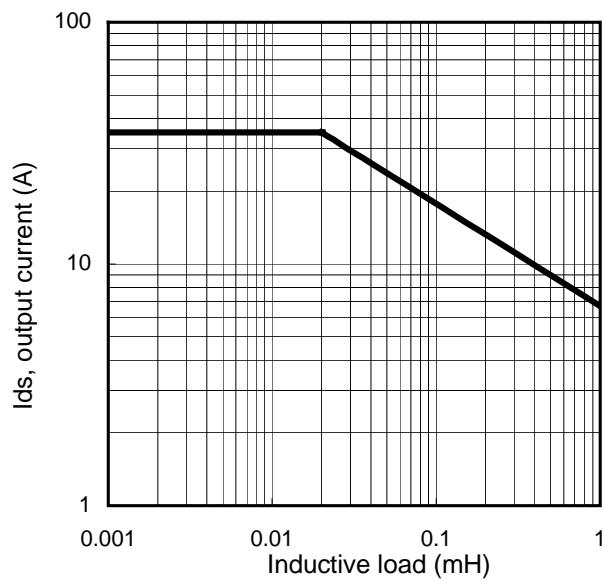


Figure 11 – Max. ouput current (A) Vs Inductive load (mH)

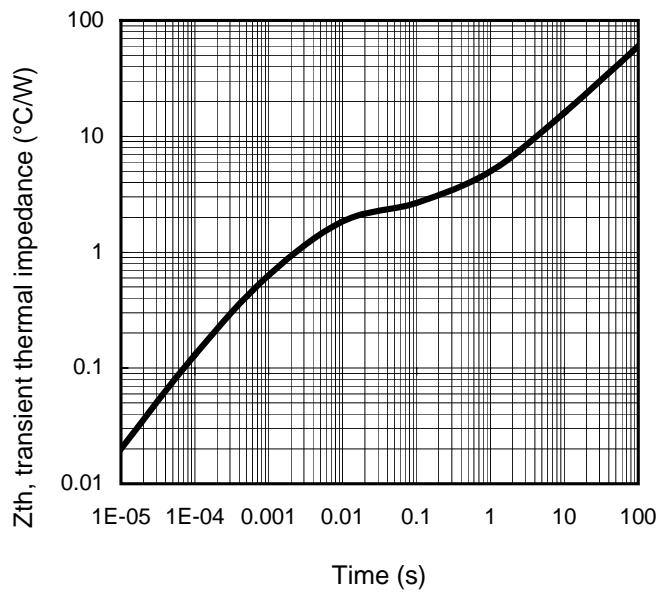
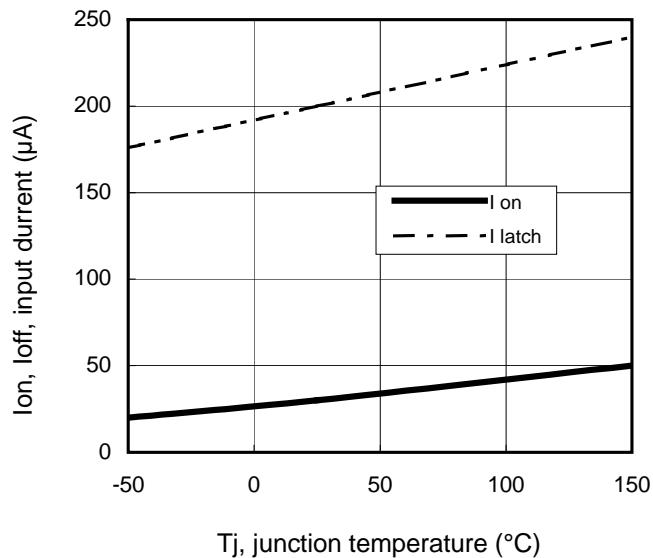
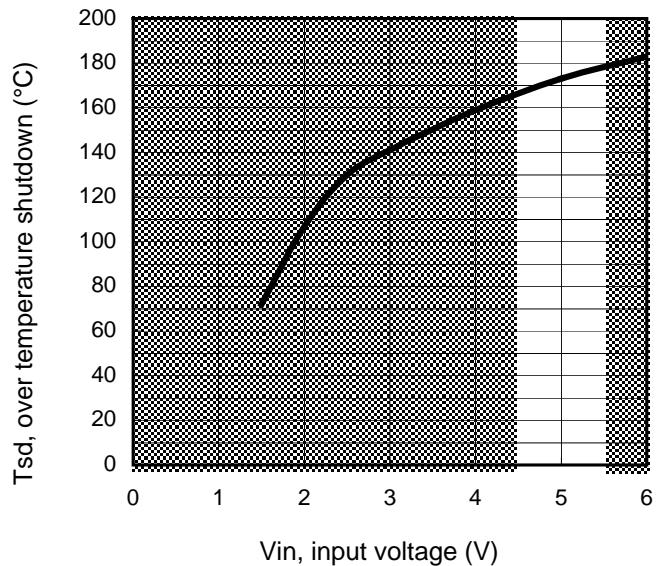


Figure 12 – Transient thermal impedance ($^{\circ}$ C/W) Vs time (s)

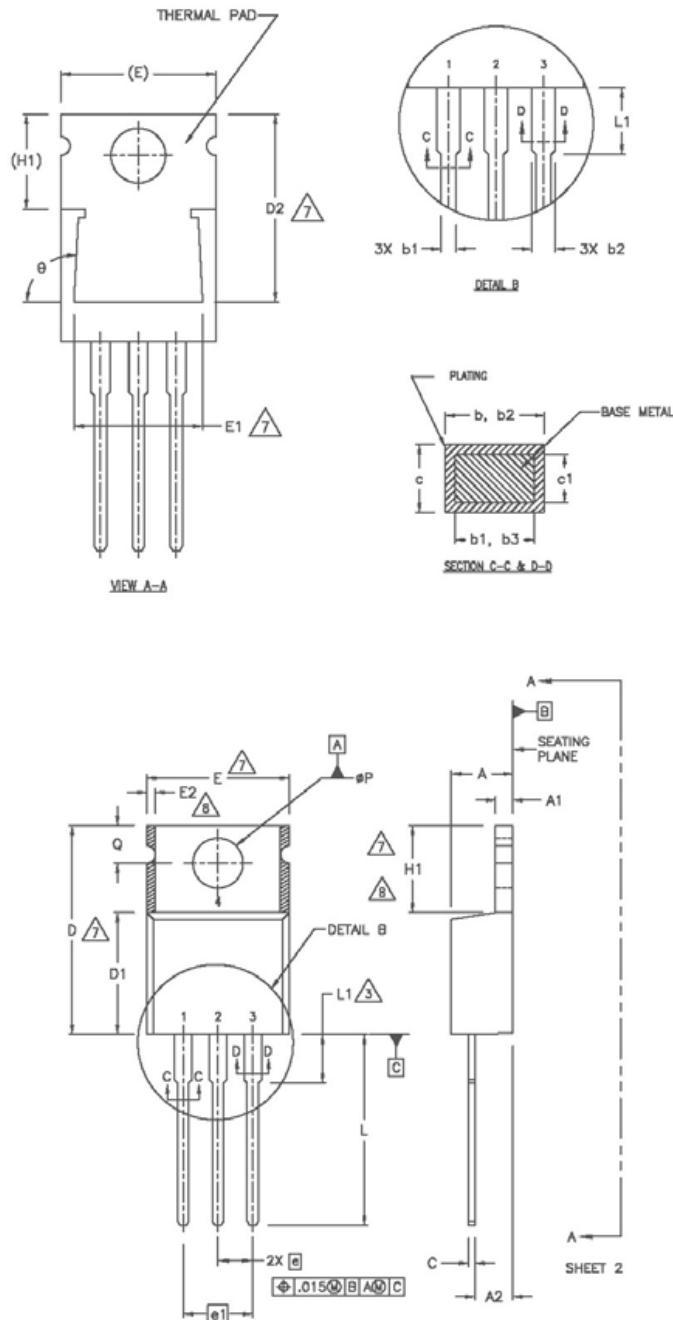


**Figure 13 – Input current (μA) On and Off
Vs junction temperature (°C)**



**Figure 14 – Over temperature shutdown (°C)
Vs input voltage (V)**

Case Outline – TO-220 AB – Automotive Q100 PbF qualified

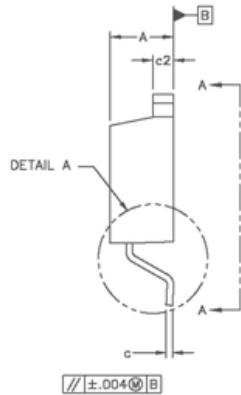
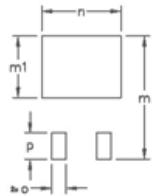
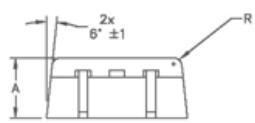
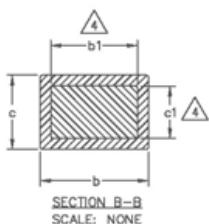
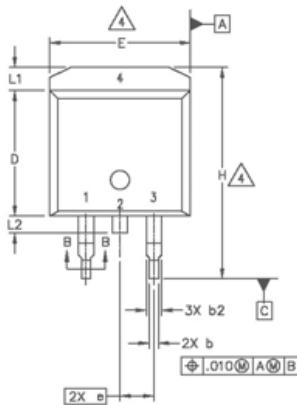
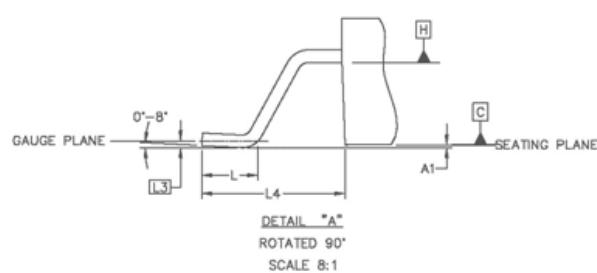
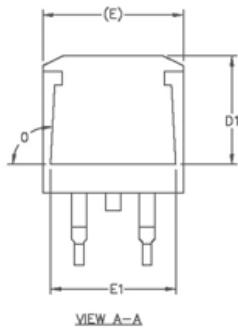


SYMBOL	DIMENSIONS				NOTES	
	MILLIMETERS		INCHES			
	MIN.	MAX.	MIN.	MAX.		
A	3.56	4.82	.140	.190		
A1	0.51	1.40	.020	.055		
A2	2.04	2.92	.080	.115		
b	0.38	1.01	.015	.040		
b1	0.38	0.96	.015	.038	5	
b2	1.15	1.77	.045	.070		
b3	1.15	1.73	.045	.068		
c	0.36	0.61	.014	.024		
c1	0.36	0.56	.014	.022	5	
D	14.22	16.51	.560	.650	4	
D1	8.38	9.02	.330	.355		
D2	12.19	12.88	.480	.507	7	
E	9.66	10.66	.380	.420	4,7	
E1	8.38	8.89	.330	.350	7	
e	2.54 BSC		.100 BSC			
e1	5.08		.200 BSC			
H1	5.85	6.55	.230	.270	7,8	
L	12.70	14.73	.500	.580		
L1	—	6.35	—	.250	3	
øP	3.54	4.08	.139	.161		
Q	2.54	3.42	.100	.135		
ø	90°-93°		90°-93°			

NOTES:

- 1 DIMENSIONING AND TOLERANCING PER ASME Y14.5 M- 1994.
- 2 DIMENSIONS ARE SHOWN IN INCHES [MILLIMETERS].
- 3 LEAD DIMENSION AND FINISH UNCONTROLLED IN L1.
- 4 DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED .005" (.127) PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY.
- 5 DIMENSION b1 & c1 APPLY TO BASE METAL ONLY.
- 6 CONTROLLING DIMENSION : INCHES.
- 7 THERMAL PAD CONTOUR OPTIONAL WITHIN DIMENSIONS E,H1,D2 & E1 AND SINGULATION IRREGULARITIES ARE ALLOWED.
- 8 DIMENSION E2 X H1 DEFINE A ZONE WHERE STAMPING AND SINGULATION IRREGULARITIES ARE ALLOWED.
- 9 LEADS AND DRAIN ARE PLATED WITH 100% Sn

Case Outline - D²Pak (SMD-220) - Automotive Q100 PbF MSL1 qualified

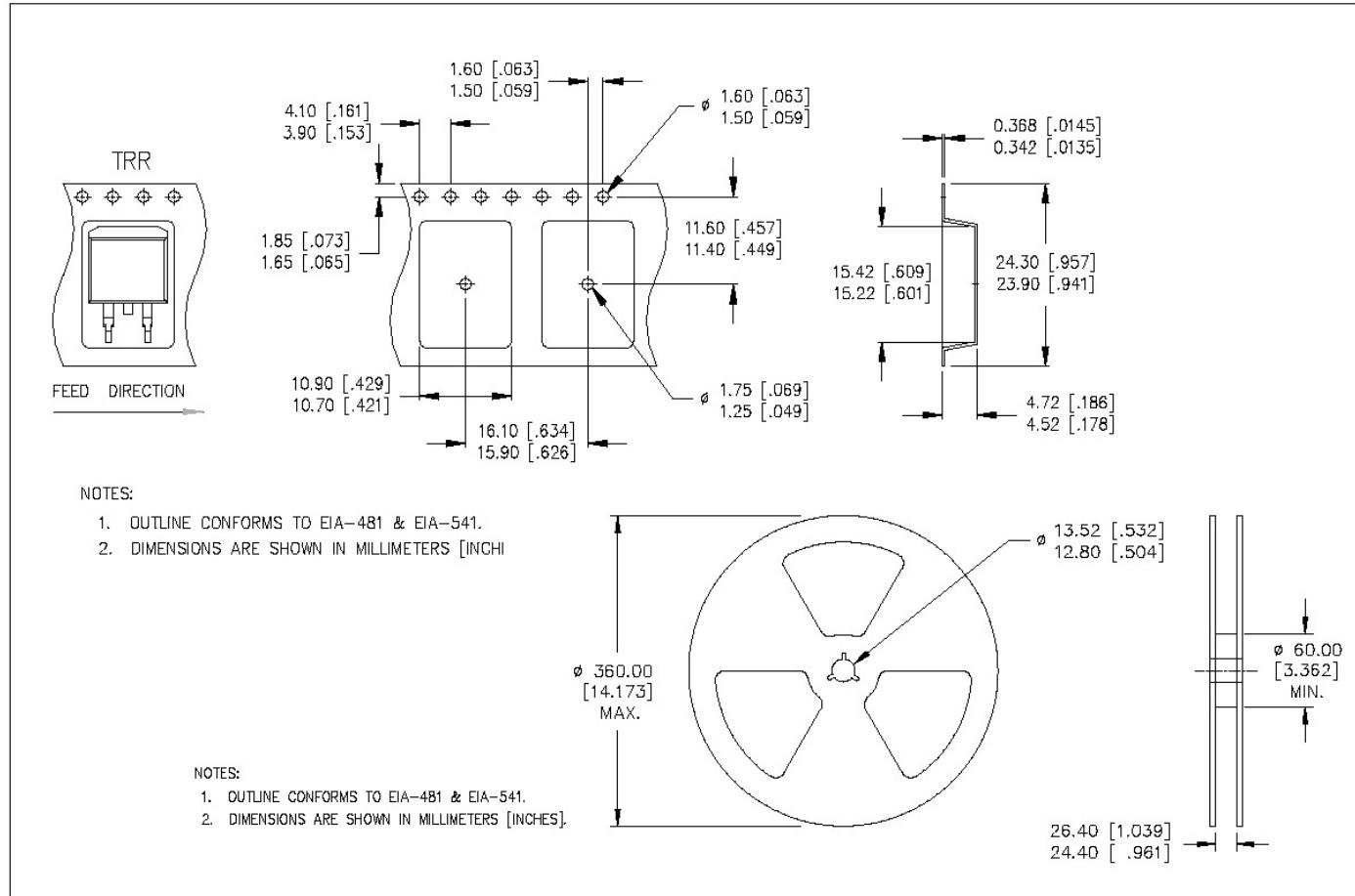


SYMBOL	DIMENSIONS		NOTES	
	MILLIMETERS			
	MIN.	MAX.		
A	4.06	4.83	.160 .190	
A1	0.00	0.254	.000 .010	
b	0.51	0.99	.020 .039	
b1	0.51	0.89	.020 .035	
b2	1.14	1.78	.045 .070	
c	0.38	0.74	.015 .029	
c1	0.38	0.58	.015 .023	
c2	1.14	1.65	.045 .065	
D	8.51	9.65	.335 .380	
D1	6.86	10.67	.270 .380	
E	9.65	10.67	.380 .420	
E1	6.22		.245	
e	2.54	BSC	.100 BSC	
H	14.61	15.88	.575 .625	
L	1.78	2.79	.070 .110	
L1		1.65	.065	
L2	1.27	1.78	.050 .070	
L3	0.25	BSC	.010 BSC	
L4	4.78	5.28	.188 .208	
m	17.78		.700	
m1	8.89		.350	
n	11.43		.450	
o	2.08		.082	
p	3.81		.150	
R	0.51	0.71	.020 .028	
θ	90°	93°	90° 93°	

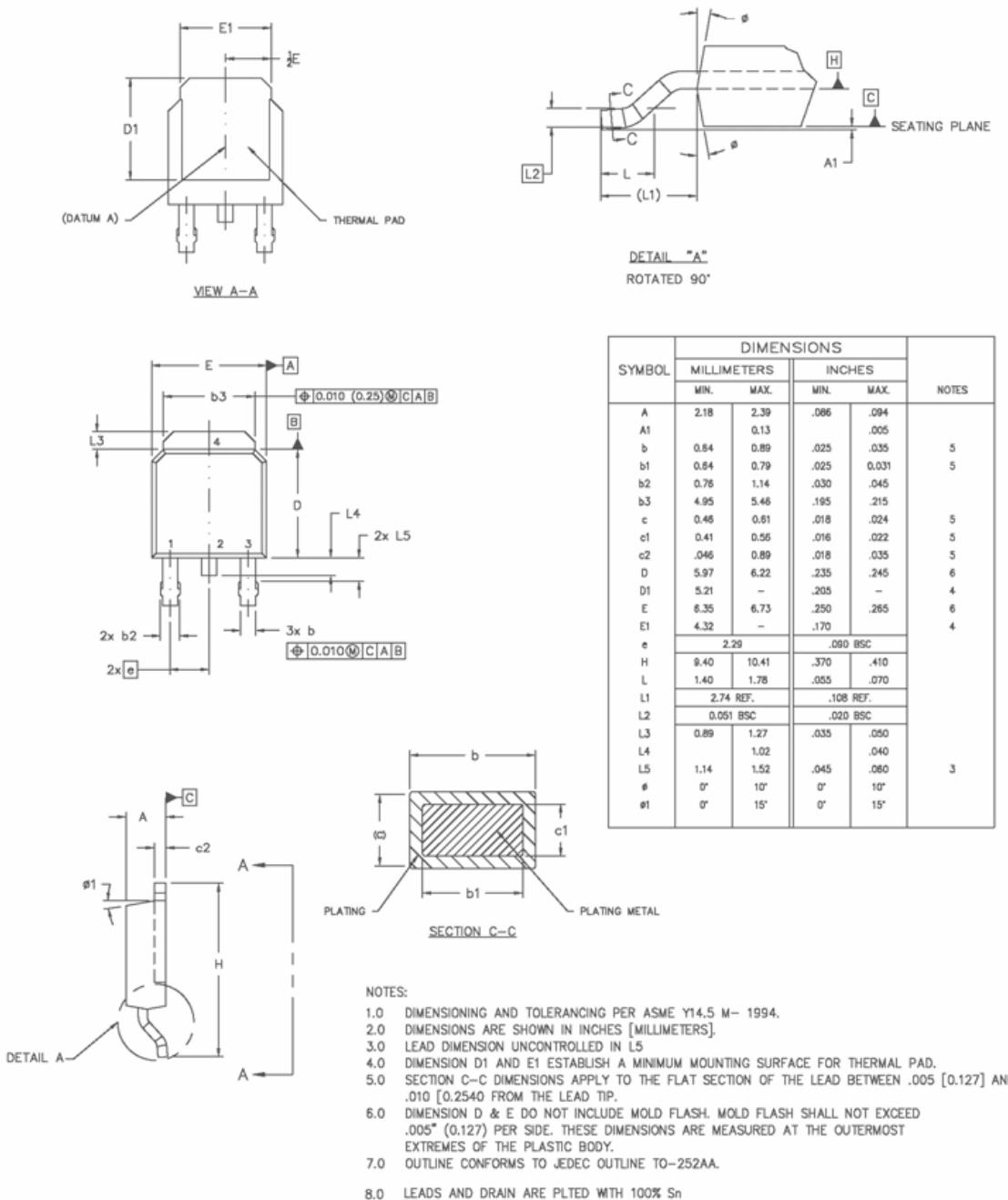
NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994
2. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
3. DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.127 [.005"] PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTMOST EXTREMES OF THE PLASTIC BODY.
4. DIMENSION b1 AND c1 APPLY TO BASE METAL ONLY.
5. CONTROLLING DIMENSION: INCH.
6. LEADS & DRAIN CONTACT ARE PLATED : 100% Sn

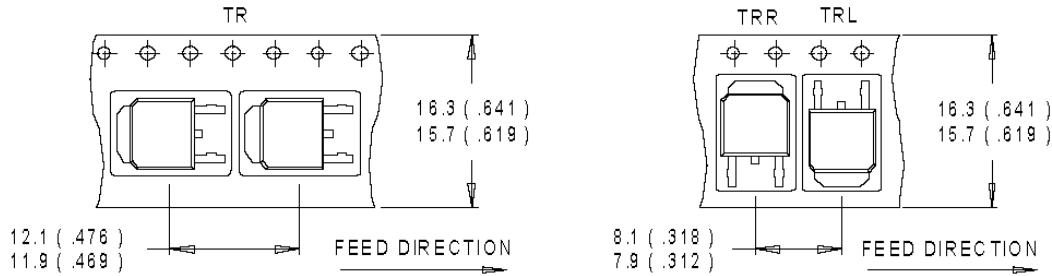
Tape & Reel - D²Pak (SMD220)



Case Outline – D-Pak - Automotive Q100 PbF MSL1 qualified

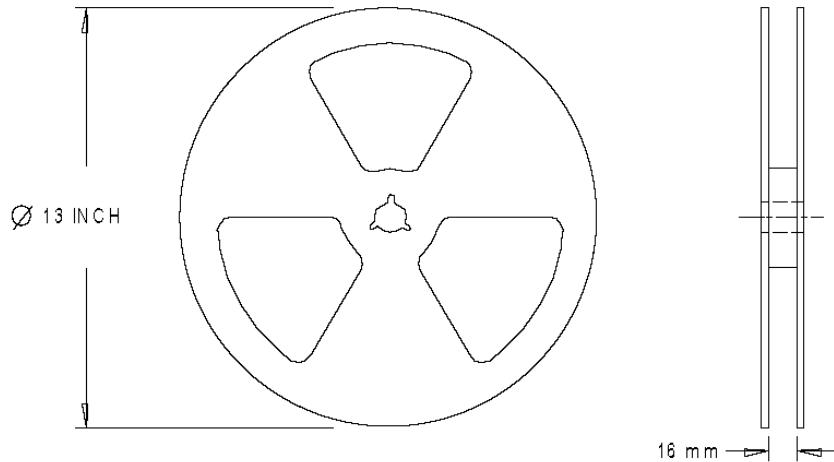


Tape & Reel - D-Pak



NOTES :

1. CONTROLLING DIMENSION : MILLIMETER.
2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS (INCHES).
3. OUTLINE CONFORMS TO EIA-481 & EIA-541.



NOTES :

1. OUTLINE CONFORMS TO EIA-481.

Dimensions are shown in millimeters (inches)

International
IR Rectifier

IR WORLD HEADQUARTERS: 233 Kansas St., El Segundo, California 90245 Tel: (310) 252-7105
 Data and specifications subject to change without notice.
This product is designed and qualified for the Automotive [Q100] market.
TO220, D2Pak and Dpak is MSL1 qualified.
 06/15/2006

Note: For the most current drawings please refer to the IR website at:
<http://www.irf.com/package/>