

BSP75G 60V self-protected low-side IntelliFET[™] MOSFET switch

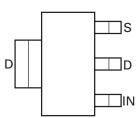
Summary

Continuous drain source voltage V_{DS} =60VOn-state resistance550m Ω Nominal load current1.4A (V_{IN} = 5V)Clamping energy550mJ



Description

Self-protected low side MOSFET. Monolithic over temperature, over current, over voltage (active clamp) and ESD protected logic level power MOSFET intended as a general purpose switch.



The tab is connected to the drain pin, and must

be electrically isolated from the source pin.

Connection of significant copper to the tab is recommended for best thermal performance.

Features

Note:

- Short circuit protection with auto restart
- Over-voltage protection (active clamp)
- Thermal shutdown with auto restart
- Over-current protection
- Input protection (ESD)
- High continuous current rating
- · Load dump protection (actively protects load)
- · Logic level input

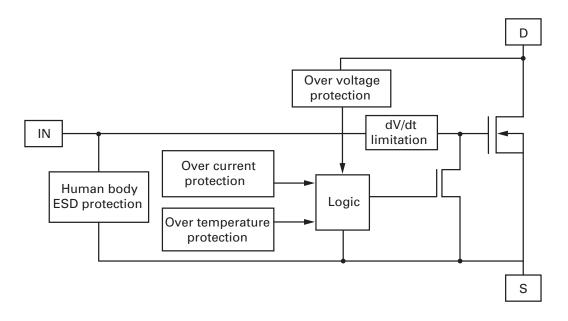
Ordering information

Device	Reel size (inches)	Tape width (mm)	Quantity per reel
BSP75GTA	7	12mm embossed	1,000
BSP75GTC	13	12mm embossed	4,000

Device marking

BSP75G

Functional block diagram



Applications

- Especially suited for loads with a high in-rush current such as lamps and motors.
- All types of resistive, inductive and capacitive loads in switching applications.
- μC compatible power switch for 12V and 24V DC applications.
- Automotive rated.
- · Replaces electromechanical relays and discrete circuits.
- Linear mode capability the current-limiting protection circuitry is designed to de-activate at low Vds, in order not to compromise the load current during normal operation. The design maximum DC operating current is therefore determined by the thermal capability of the package/board combination, rather than by the protection circuitry.

Absolute maximum ratings

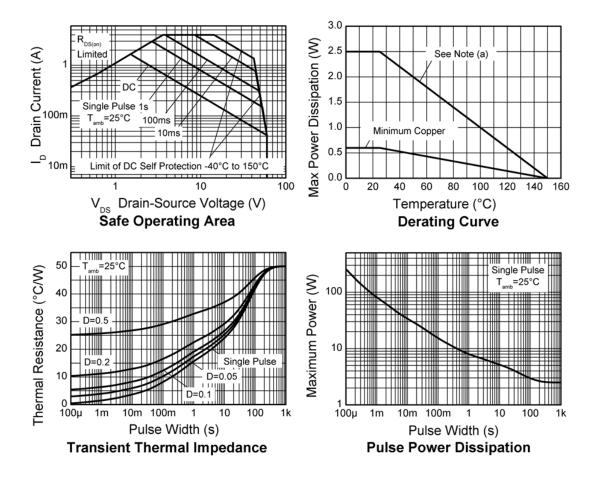
Parameter	Symbol	Limit	Unit
Continuous drain-source voltage	V _{DS}	60	V
Drain-source voltage for short circuit protection	V _{DS(SC)}	36	V
Continuous input voltage	V _{IN}	-0.2 +10	V
Peak input voltage	V _{IN}	-0.2 +20	V
Operating temperature range	Т _ј ,	-40 to +150	°C
Storage temperature range	T _{stg}	-55 to +150	°C
Power dissipation at $T_A = 25^{\circ}C^{(a)}$	P _D	2.5	W
Continuous drain current @ V_{IN} =10V; T _A =25°C ^(a)	I _D	1.6	А
Continuous drain current @ V_{IN} =5V; T _A =25°C ^(a)	۱ _D	1.4	А
Pulsed drain current @ V _{IN} =10V	I _{DM}	5	А
Continuous source current (body diode) ^(a)	ا _S	3	А
Pulsed source current (body diode)	۱ _S	5	А
Unclamped single pulse inductive energy	E _{AS}	550	mJ
Load dump protection	V _{LoadDump}	80	V
Electrostatic discharge (human body model)	V _{ESD}	4000	V
DIN humidity category, DIN 40 040		E	
IEC climatic category, DIN IEC 68-1		40/150/56	
Thermal resistance			
Parameter	Symbol	Limit	Unit
Junction to ambient ^(a)	$R_{\Theta JA}$	50	°C/W
Junction to ambient ^(b)	$R_{\Theta JA}$	24	°C/W
Junction to ambient ^(c)	$R_{\Theta JA}$	208	°C/W

NOTES:

(a) For a device surface mounted on 37mm x 37mm x 1.6mm FR4 board with a high coverage of single sided 2oz weight copper. (b) For a device surface mounted on FR4 board and measured at t<=10s.

(c) For a device mounted on FR4 board with the minimum copper required for electrical connections.

Characteristics



Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Static characteristics						
Drain-source clamp voltage	V _{DS(AZ)}	60	70	75	V	I _D =10mA
Off-state drain current	I _{DSS}		0.1	3	μA	V _{DS} =12V, V _{IN} =0V
Off-state drain current	I _{DSS}		3	15	μA	V _{DS} =32V, V _{IN} =0V
Input threshold voltage ^(*)	V _{IN(th)}	1	2.1		V	V _{DS} =V _{GS} , I _D =1mA
Input current	I _{IN}		0.7	1.2	mA	V _{IN} =+5V
Input current	I _{IN}		1.5	2.7	mA	V _{IN} =+7V
Input current	I _{IN}		4	7	mA	V _{IN} =+10V
Static drain-source on-state resistance	R _{DS(on)}		520	675	mΩ	V _{IN} =+5V, I _D =0.7A
Static drain-source on-state resistance	R _{DS(on)}		385	550	mΩ	V _{IN} =+10V, I _D =0.7A
Current limit ^(†)	I _{D(LIM)}	0.7	1.1	1.75	А	V _{IN} =+5V, V _{DS} >5V
Current limit ^(†)	I _{D(LIM)}	2	3	4	А	V _{IN} =+10V, V _{DS} >5V
Dynamic characteristics		•				
Turn-on time (V _{IN} to 90% I_D)	t _{on}		2.2	10	μS	R _L =22Ω, V _{DD} =12V, V _{IN} =0 to +10V
Turn-off time (V _{IN} to 90% I_D)	t _{off}		13	20	μS	R _L =22Ω, V _{DD} =12V, V _{IN} =+10V to 0V
Slew rate on (70 to 50% $\mathrm{V}_\mathrm{DD})$	$-dV_{DS}/dt_{on}$		10	20	V/µs	R _L =22Ω, V _{DD} =12V, V _{IN} =0 to +10V
Slew rate off (50 to 70% $\mathrm{V}_\mathrm{DD})$	$\mathrm{dV}_{\mathrm{DS}}/\mathrm{dt}_{\mathrm{off}}$		3.2	10	V/µs	R _L =22Ω, V _{DD} =12V, V _{IN} =+10V to 0V
Protection functions (‡)		•	•	•		·
Required input voltage for over temperature protection	V _{PROT}	4.5			V	
Thermal overload trip temperature	Τ _{JT}	150	175		°C	
Thermal hysteresis			10		°C	
Unclamped single pulse inductive energy Tj=25°C	E _{AS}	550			mJ	I _{D(ISO)} =0.7A, V _{DD} =32V
Unclamped single pulse inductive energy Tj=150°C	E _{AS}	200			mJ	I _{D(ISO)} =0.7A, V _{DD} =32V
Inverse diode						
Source drain voltage	V _{SD}				1	V _{IN} =0V, -I _D =1.4A

Electrical characteristics (at Tamb = 25°C unless otherwise stated)

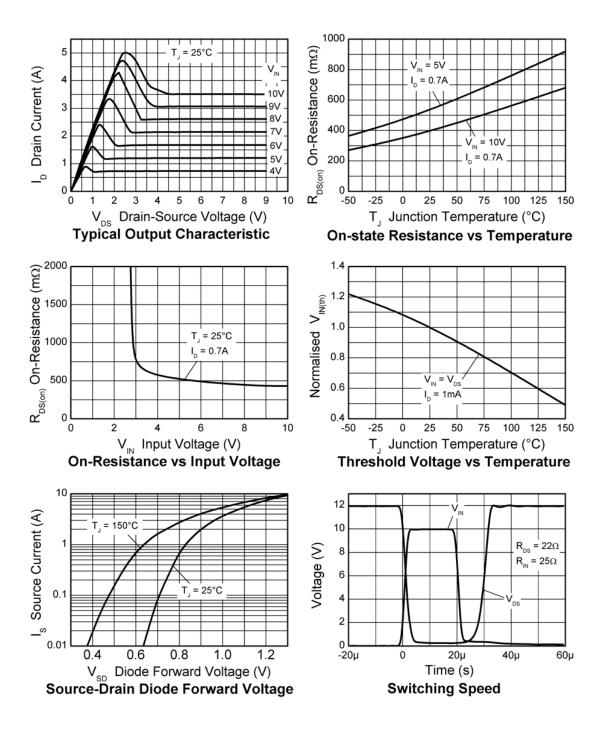
NOTES:

(*) Protection features may operate outside spec for $V_{\rm IN}{<}4.5V.$

(†) The drain current is limited to a reduced value when V_{DS} exceeds a safe level.

(‡) Integrated protection functions are designed to prevent IC destruction under fault conditions described in the datasheet. Fault conditions are considered as "outside" normal operating range. Protection functions are not designed for continuous, repetitive operation.

Characteristics

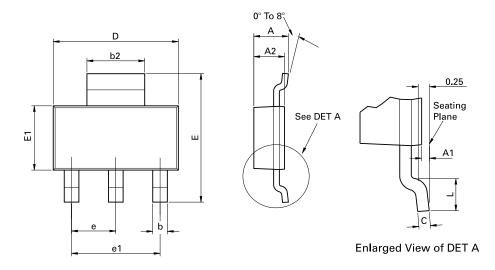


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Package outline - SOT223



Conforms to JEDEC TO-261 AA Issue B

Dim.	Millin	Millimeters		Inches Dim.		Millin	neters	Inc	hes
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
А	-	1.80	-	0.071	е	2.30	BSC	0.090	5 BSC
A1	0.02	0.10	0.0008	0.004	e1	4.60	BSC	0.181	BSC
b	0.66	0.84	0.026	0.033	E	6.70	7.30	0.264	0.287
b2	2.90	3.10	0.114	0.122	E1	3.30	3.70	0.130	0.146
С	0.23	0.33	0.009	0.013	L	0.90	-	0.355	-
D	6.30	6.70	0.248	0.264	-	-	-	-	-

Note: Controlling dimensions are in millimeters. Approximate dimensions are provided in inches

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