### 1.2A White LED Regulating Charge Pump for Camera Flashes and Movie Lights

## General Description

The MAX1577Y/MAX1577Z charge pumps drive white LEDs, including camera strobes, with regulated current up to 1.2A (guaranteed to 800 mA ). The very low openloop output resistance allows high flash brightness, even from a low battery input voltage. The adaptive $1 x / 2 x$ regulating charge-pump operation provides high efficiency (up to 92\%) while in movie mode or backlighting. Fast $(1 \mathrm{MHz})$ switching allows the use of tiny external components.
The MAX1577Y/MAX1577Z use an external resistor to set the full-scale LED current. Two enable inputs (EN1 and EN2) provide simple on/off control and LED current of $20 \%, 33 \%$, or $100 \%$ full scale. If there is less than 24 mV across the external resistor, the output voltage is regulated. The MAX1577Z has fixed 5.1 V output-voltage regulation, while the MAX1577Y has logic-controlled output voltage of $3.4 \mathrm{~V}, 4.6 \mathrm{~V}$, or 5.1 V for backlighting, keyboard, and RGB applications.
The MAX1577Y/MAX1577Z are available in an 8-pin, $3 \mathrm{~mm} \times 3 \mathrm{~mm}$ TDFN package ( 0.8 mm max height).

## Applications

White LED Flashes, Strobes, and Movie Lights
LCD Backlighting
Camera Phones, Cell Phones, and Smart Phones
PDAs, Digital Cameras, and Camcorders

Typical Operating Circuit


- Up to 1.2A Output Drive Capability ( 800 mA Guaranteed)
- Up to 92\% Pled / Pbatt Efficiency for Movie Mode
- Flexible Brightness Control

Two-Bit Logic for 100\%, 33\%, 20\%, and Off PWM for 20\% to 100\%

- 3\% Current Regulation Over Line and Temperature
- Current or Voltage Regulation for Flash and Backlighting (MAX1577Y)
- Current Regulation for Flash (MAX1577Z)
- Low Ripple and EMI
- 2.7V to 5.5V Supply Voltage Range
- Soft-Start Limits Inrush Current
- Output-Voltage Regulation Mode
- Thermal-Shutdown Protection
- 8-Pin 3mm x 3mm TDFN Package

Ordering Information

| PART | TEMP RANGE | PIN- <br> PACKAGE | TOP <br> MARK |
| :---: | :---: | :--- | :---: |
| MAX1577YETA | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 8 TDFN <br> $($ T833-2) | AMG |
| MAX1577ZETA | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 8 TDFN <br> $($ T833-2) | AMQ |

Pin Configuration


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## ABSOLUTE MAXIMUM RATINGS

| IN, OUT, EN1, EN2 to GND ... | .........-0.3V to +6.0V |
| :---: | :---: |
| OUT to GND During Shutdown | -0.3 V to ( V IN +0.3 V ) |
| OUT to CS | -0.6 V to +0.6 V |
| CS to GND | -0.3V to (VOUT + 0.3V) |
| CN to GND | -0.3V to ( $\mathrm{V}_{\text {IN }}+0.3 \mathrm{~V}$ ) |
| CP to GND .....-0.3V to the G | OUT + 1V) or (VIN + 1V) |
| OUT Short Circuit to GND . | .....Continuous |

Continuous Power Dissipation ( $\mathrm{T}_{\mathrm{A}}=+70^{\circ} \mathrm{C}$ ) 8-Pin TDFN (derate $18.2 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$ above $+70^{\circ} \mathrm{C}$ ) .......... 1455 mW Operating Temperature Range ........................... $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ Junction Temperature ...................................................... $+150^{\circ} \mathrm{C}$ Storage Temperature Range ............................. $65^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$ Lead Temperature (soldering, 10s) ................................. $+300^{\circ} \mathrm{C}$

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## ELECTRICAL CHARACTERISTICS

$\left(\mathrm{VIN}=3.6 \mathrm{~V}\right.$, circuit of Figure 2, $\mathrm{EN} 1=\mathrm{EN} 2=\mathrm{IN}, \mathrm{RCS}=60 \Omega, \mathrm{CIN}=4.7 \mu \mathrm{~F}, \mathrm{C} 1=2.2 \mu \mathrm{~F}, \mathrm{COUT}=10 \mu \mathrm{~F}, \mathrm{~T}_{\mathrm{A}}=-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$, unless otherwise noted. Typical values are at $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$.) (Note 1)


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## ELECTRICAL CHARACTERISTICS (continued)

$\left(\mathrm{VIN}=3.6 \mathrm{~V}\right.$, circuit of Figure 2, $\mathrm{EN} 1=\mathrm{EN} 2=\mathrm{IN}, \mathrm{RCS}=60 \Omega, \mathrm{CIN}=4.7 \mu \mathrm{~F}, \mathrm{C} 1=2.2 \mu \mathrm{~F}, \mathrm{COUT}=10 \mu \mathrm{~F}, \mathrm{~T}_{\mathrm{A}}=-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$, unless otherwise noted. Typical values are at $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$.) (Note 1)

| PARAMETER | CONDITIONS |  | MIN | TYP | MAX | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| EN_ Logic-High Voltage | $\mathrm{V}_{\mathrm{IN}}=4.2 \mathrm{~V}$ to 5.5 V |  | 1.6 |  |  | V |
|  | $\mathrm{V}_{\text {IN }}=2.7 \mathrm{~V}$ to 4.2V |  | 1.4 |  |  |  |
| EN_ Logic-Low Voltage | $\mathrm{V}_{\text {IN }}=2.7 \mathrm{~V}$ to 5.5 V |  |  |  | 0.4 | V |
| Logic Input Current | $\mathrm{V}_{E N_{-}}=0 \mathrm{~V}$ or 5.5 V | $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$ |  | 0.02 | 1 | $\mu \mathrm{A}$ |
|  |  | $\mathrm{T}_{\mathrm{A}}=+85^{\circ} \mathrm{C}$ |  | 0.02 |  |  |
| Thermal-Shutdown Threshold | TJ rising |  |  | +160 |  | ${ }^{\circ} \mathrm{C}$ |
| Thermal-Shutdown Hysteresis |  |  |  | 20 |  | ${ }^{\circ} \mathrm{C}$ |

Note 1: Limits are $100 \%$ production tested at $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$. Limits over the operating temperature range are guaranteed by design.

Typical Operating Characteristics
( V IN $=3.6 \mathrm{~V}$, circuit of Figure $2, \mathrm{R} C S=0.27 \Omega$, Luxeon LXCL-PWF1 LED, $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$, unless otherwise noted.)


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## Typical Operating Characteristics (continued)

( V IN $=3.6 \mathrm{~V}$, circuit of Figure 2, RCS $=0.27 \Omega$, Luxeon LXCL-PWF1 LED, $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$, unless otherwise noted.)


STARTUP AND SHUTDOWN RESPONSE





### 1.2A White LED Regulating Charge Pump for Camera Flashes and Movie Lights

Pin Description

| PIN | NAME | FUNCTION |
| :---: | :---: | :---: |
| 1 | EN1 | Control Logic Input (see Table 1) |
| 2 | CP | Transfer Capacitor Positive Connection. Connect a $1 \mu \mathrm{~F}$ capacitor (or $2.2 \mu \mathrm{~F}$ for IOUT $>600 \mathrm{~mA}$ ) from CP to CN. |
| 3 | CN | Transfer Capacitor Negative Connection. Connect a $1 \mu \mathrm{~F}$ capacitor (or $2.2 \mu \mathrm{~F}$ for IOUT $>600 \mathrm{~mA}$ ) from CP to CN. |
| 4 | EN2 | Control Logic Input (see Table 1) |
| 5 | GND | Ground |
| 6 | IN | Input Supply Voltage. Connect IN to a battery or supply voltage from 2.7V to 5.5V. Connect a $4.7 \mu \mathrm{~F}$ ceramic capacitor from IN to GND. |
| 7 | CS | Current-Sense Input. Connect a current-sense resistor from OUT to CS to set the output current. Connect the anodes of the LEDs to CS and the cathodes to GND. |
| 8 | OUT | Output. Connect a $10 \mu \mathrm{~F}$ ceramic capacitor from OUT to GND. In shutdown, OUT is pulled to GND with an internal $5 \mathrm{k} \Omega$ resistor. |
| - | EP | Exposed Pad. Connect the exposed pad to GND directly under the IC. |

Table 1. Enable, Dimming, Voltage Mode, and Overvoltage Protection

| EN1 | EN2 | DIMMING LEVEL | CS THRESHOLD <br> (Vout- VCs) | VOLTAGE MODE |  | OVP |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  | MAX1577Z | MAX1577Y | MAX1577Z | MAX1577Y |
| 0 | 0 | Shutdown | - | - | - | - | - |
| 0 | 1 | $20 \%$ Current | 60 mV | 5.10 V | 3.40 V | 5.30 V | 5.15 V |
| 1 | 0 | $33 \%$ Current | 100 mV | 5.10 V | 4.60 V | 5.30 V | 5.17 V |
| 1 | 1 | $100 \%$ Current | 300 mV | 5.10 V | 5.10 V | 5.30 V | 5.30 V |

Note: A " 0 " indicates EN_ is logic low or connected to GND. A " 1 " indicates EN_ is logic high or connected to IN.

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Figure 1. Functional Diagram
$\qquad$

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Figure 2. Current Regulation with the MAX1577Z


Figure 3. Voltage Regulation with the MAX1577Y

# 1.2A White LED Regulating Charge Pump for Camera Flashes and Movie Lights 



Figure 4. Flash and Backlighting with the MAX1577Y

## Detailed Description

1x LDO to 2x Mode Switchover When VIN is greater than VOUT, the MAX1577Y/ MAX1577Z are in 1x LDO mode and regulate the current through RCS. As VIN decreases and the LDO approaches dropout, the $2 x$ charge pump starts boosting the output to maintain the regulation current. When VIN rises enough above VOUT to maintain LDO regulation, the charge pump stops and the MAX1577Y/MAX1577Z switch back to the more efficient $1 \times$ LDO mode.

## Soft-Start

The MAX1577Y/MAX1577Z include soft-start circuitry to limit inrush current at turn-on. When starting up with an output voltage that is not near the input voltage, the output capacitor is charged directly from the input with a ramped current source (with no charge-pump action) until the output voltage approaches the input voltage. If current-mode or voltage-mode regulation is met, soft-
start is terminated and normal operation begins in $1 x$ mode. Otherwise, after $500 \mu \mathrm{~s}$, the $2 x$ charge-pump softstart operates until the voltage across the sense resistor reaches the current regulation threshold, the output voltage is in regulation, or $500 \mu$ s has elapsed, whichever occurs first. If the output is shorted to ground, soft-start is repeated until the output exceeds 2.3 V .

## Dimming and Shutdown

The MAX1577Y/MAX1577Z use two inputs (EN1 and EN2) to select between three brightness levels and lowpower shutdown mode (see Table 1). By regulating the voltage across the current-sense resistor (VOUT - VCS) to one of three levels, the MAX1577Y/MAX1577Z control the output current, thus providing the three-level dimming. In shutdown mode, the IC is turned off, reducing the supply current to $0.01 \mu \mathrm{~A}$ (typ). When the MAX1577Y/MAX1577Z are shut down, C1 is discharged and the output is pulled to GND through an internal $5 \mathrm{k} \Omega$ resistor.

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## Overvoltage Protection

Output overvoltage protection (OVP) protects the IC and other components in case the output is open circuit. The MAX1577Z always limits the output voltage to 5.3 V . With the MAX1577Y, the output OVP voltage varies slightly depending on the state of the EN_inputs (see Table 1).

## Output-Voltage Regulation Mode

 When the voltage across the sense resistor (Vout VCS) is less than 24 mV , the MAX1577Y/MAX1577Z regulate the output voltage. For applications requiring a fixed output voltage instead of current regulation, simply connect CS directly to OUT as shown in Figure 3. The MAX1577Z regulates to a fixed 5.1 V . The adjustable regulation voltage ( $3.4 \mathrm{~V}, 4.6 \mathrm{~V}$, or 5.1 V ) of the MAX1577Y depends on the state of the EN_ inputs (see Table 1).Thermal Protection Thermal protection prevents damage to the IC during overload conditions. When the die temperature exceeds $+160^{\circ} \mathrm{C}$, the MAX1577Y/MAX1577Z turn off until the die cools by $20^{\circ} \mathrm{C}$. Once the die has cooled, the MAX1577Y/MAX1577Z turn on and initiate soft-start. During a continuous overload, this produces a pulsed output. Continous operation at high-output-current settings may induce thermal protection.

## Capacitor Selection

Ceramic capacitors are recommended due to their low equivalent series resistance (ESR), small size, and low cost. Select capacitors that maintain their capacitance over temperature and DC bias. Typically X5R, X7R, or better ceramic capacitors perform well.
A $4.7 \mu \mathrm{~F}$ input capacitor and $10 \mu \mathrm{~F}$ output capacitor are recommended for most applications. Larger values can be used to reduce ripple.
For the transfer capacitor (C1), a $1 \mu \mathrm{~F}$ capacitor is suitable for output currents up to 600 mA . For output currents between 600 mA and 1200 mA , use a $2.2 \mu \mathrm{~F}$ capacitor.

## Input Ripple

 In 1x LDO mode there is no charge-pump switching, and thus very little input ripple. The input ripple in $2 x$ charge-pump mode depends on the input source impedance. Typical waveforms showing the ripple in 1 x and $2 x$ mode are in the Typical Operating Characteristics. In sensitive applications, increase the input capacitance to reduce input ripple.
## Applications Information

## Setting the Output Current

For applications requiring a regulated output current, see the circuit of Figure 2. The output regulation current is set by Rcs as follows:
RCS = (VOUT - VCS) / IOUT
where IOUT is the output current (up to 1200 mA ) and (VOUT - VCS) is the current-sense threshold (see Table 1). For camera-flash applications with a short pulse duration, a 0.25 W resistor is recommended.

Setting the Output Voltage To regulate the output voltage instead of the current, connect CS directly to the output as shown in Figure 3. In this configuration the MAX1577Z regulates the output to 5.1 V . The output voltage of the MAX1577Y is set to $5.1 \mathrm{~V}, 4.6 \mathrm{~V}$, or 3.4 V using the EN1 and EN2 inputs as shown in Table 1.

Driving Camera Strobe/Flash In applications that do not require dimming, such as a camera strobe/flash, connect EN1 and EN2 together and drive as a single on/off control input. Pulse this input high to generate a strobe output.

## PWM Dimming

To vary the brightness from $20 \%$ to $100 \%$ using pulsewidth modulation (PWM), hold EN2 high and apply the PWM signal (up to 5 kHz ) to EN1. Holding EN2 high keeps the part enabled and prevents repeated softstarts. The duty cycle of the PWM signal applied to EN1 controls the brightness, where $0 \%$ duty cycle corresponds to $20 \%$ brightness and $100 \%$ duty cycle corresponds to $100 \%$ brightness. A PWM signal with a logic-high voltage between 1.6 V and 5.5 V can be safely used regardless of the input voltage. The voltage of the PWM signal does not affect the brightness level.
For highest movie-mode efficiency, it is best to choose Rcs so 0\% PWM duty cycle provides the desired movie-mode current. Then, the flash current level is adjusted by the PWM duty cycle. When the moviemode current is more than $33 \%$ of the flash current, it is more efficient to apply the PWM dimming signal to EN2 while holding EN1 high.

## Ballast Resistors for Multiple LEDs

When using multiple parallel LEDs, add a ballast resistor in series with each LED to help balance the current between the LEDs. When using current regulation

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(Figure 2), the ballast resistors must be small enough to prevent triggering the overvoltage protection:

$$
\text { RBALLAST < (5.1V - } \left.\mathrm{V}_{\mathrm{F}}\right) / \text { ILED }
$$

where ILED is the current through each LED and $V_{F}$ is the forward voltage of the LED at ILED. Within this limit, larger values of RBALLAST improve LED-to-LED matching but also increase the input voltage where the $2 x$ charge pump makes the transition to the more efficient 1x LDO mode.

When using voltage regulation (Figure 3), select the ballast resistors based on the full brightness setting as follows:
With EN1 high and EN2 high:

$$
\text { RBALLAST }=\left(5.1 \mathrm{~V}-\mathrm{V}_{\mathrm{F}}\right) / \text { ILED }
$$

The LED current at the dimmed settings are calculated as follows:
with EN1 high and EN2 low:

$$
\text { ILED }=\left(4.6 \mathrm{~V}-\mathrm{V}_{\mathrm{F}}\right) / \text { RBALLAST }
$$

with EN1 low and EN2 high:

$$
\text { ILED }=\left(3.4 \mathrm{~V}-\mathrm{V}_{\mathrm{F}}\right) / \text { RBALLAST }
$$

Note that $V_{F}$ varies as a function of ILED. Typically, LED manufacturers provide a plot of forward voltage ( $V_{F}$ ) vs. forward current (ILED) in the LED data sheet.

PC Board Layout and Routing The MAX1577Y/MAX1577Z are high-frequency switched-capacitor regulators. For best circuit performance, use a solid ground plane and place CIN, Cout, and C1 as close to the IC as possible. Connect the exposed pad to GND directly under the IC and allow sufficient copper area for cooling. Refer to the MAX1577Z evaluation kit for an example PC board layout.

## Chip Information

TRANSISTOR COUNT: 2482
PROCESS: BiCMOS

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Package Information
(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information go to www.maxim-ic.com/packages.)


| PACKAGE VARIATIONS |  |  |  |  |  |  |  |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| PKG. CODE | N | D 2 | E 2 | e | JEDEC SPEC | b | $[(\mathrm{N} / 2)-1] \times \mathrm{e}$ |
| T633-1 | 6 | $1.50 \pm 0.10$ | $2.30 \pm 0.10$ | 0.95 BSC | MO229/WEEA | $0.40 \pm 0.05$ | 1.90 REF |
| T833-1 | 8 | $1.50 \pm 0.10$ | $2.30 \pm 0.10$ | 0.65 BSC | MO229/WEEC | $0.30 \pm 0.05$ | 1.95 REF |
| T1033-1 | 10 | $1.50 \pm 0.10$ | $2.30 \pm 0.10$ | 0.50 BSC | MO229 / WEED- 3 | $0.25 \pm 0.05$ | 2.00 REF |
| T1433-1 | 14 | $1.70 \pm 0.10$ | $2.30 \pm 0.10$ | 0.40 BSC | ---- | $0.20 \pm 0.03$ | 2.40 REF |
| T1433-2 | 14 | $1.70 \pm 0.10$ | $2.30 \pm 0.10$ | 0.40 BSC | ---- | $0.20 \pm 0.03$ | 2.40 REF |

NOTES:
. ALL DIMENSIONS ARE $\operatorname{IN} \mathrm{mm}$. ANGLES $\operatorname{IN}$ DEGREES.
2. COPLANARITY SHALL NOT EXCEED 0.08 mm
4. PACKAGE LENGTH/PACKAGE WIDTH ARE CONSIDERED SPECIAL CHARACTERISTIC(S).
. DRAWING CONFORMS TO JEDEC MO229, EXCEPT DIMENSIONS "D2" AND "E2",


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