# **Constant Current Regulator** & LED Driver

45 V, 30 mA ± 10%, 1.4 W Package

The linear constant current regulator (CCR) is a simple, economical and robust device designed to provide a cost-effective solution for regulating current in LEDs. The CCR is based on patent-pending Self-Biased Transistor (SBT) technology and regulates current over a wide voltage range. It is designed with a negative temperature coefficient to protect LEDs from thermal runaway at extreme voltages and currents.

The CCR turns on immediately and is at 25% of regulation with only 0.5 V Vak. It requires no external components allowing it to be designed as a high or low-side regulator. The high anode-cathode voltage rating withstands surges common in Automotive, Industrial and Commercial Signage applications. The CCR comes in thermally robust packages and is qualified to AEC-Q101 standard.

# Features

- Robust Power Package: 1.4 Watts
- Wide Operating Voltage Range
- Immediate Turn-On
- Voltage Surge Suppressing Protecting LEDs
- AEC-Q101 Qualified
- SBT (Self-Biased Transistor) Technology
- Negative Temperature Coefficient
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

# Applications

- Automobile: Chevron Side Mirror Markers, Cluster, Display & Instrument Backlighting, CHMSL, Map Light
- AC Lighting Panels, Display Signage, Decorative Lighting, Channel Lettering
- Switch Contact Wetting
- Application Note AND8391/D Power Dissipation Considerations
- Application Note AND8349/D Automotive CHMSL

### MAXIMUM RATINGS (T<sub>A</sub> = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit	
Anode-Cathode Voltage	Vak Max	45	V	
Reverse Voltage	V <sub>R</sub>	500	mV	
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	–55 to +150	°C	
ESD Rating: Human Body Model Machine Model	ESD	Class 1C Class B		

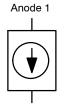
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



# **ON Semiconductor®**

http://onsemi.com

l<sub>reg(SS)</sub> = 30 mA @ Vak = 7.5 V

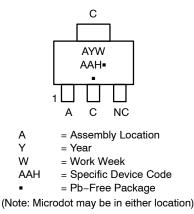


Cathode 2/4



SOT-223 CASE 318E STYLE 2





### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
NSI45030AZT1G	SOT-223 (Pb-Free)	1000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Semiconductor Components Industries, LLC, 2009 September, 2009 – Rev. 1

# ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
Steady State Current @ Vak = 7.5 V (Note 1)	I <sub>reg(SS)</sub>	27	30	33	mA
Voltage Overhead (Note 2)	Voverhead		1.8		V
Pulse Current @ Vak = 7.5 V (Note 3)	I <sub>reg(P)</sub>	28.4	31.55	34.7	mA
Capacitance @ Vak = 7.5 V (Note 4)	С		2.6		pF
Capacitance @ Vak = 0 V (Note 4)	С		6.9		pF

1. I<sub>reg(SS)</sub> steady state is the voltage (Vak) applied for a time duration ≥ 10 sec, using FR-4 @ 300 mm<sup>2</sup> 2 oz. Copper traces, in still air.

2. Voverhead = Vin – VLEDs. Voverhead is typical value for 70% I<sub>reg(SS)</sub>. 3. I<sub>reg(P)</sub> non-repetitive pulse test. Pulse width t  $\leq$  300 µsec. 4. f = 1 MHz, 0.02 V RMS.

### **THERMAL CHARACTERISTICS**

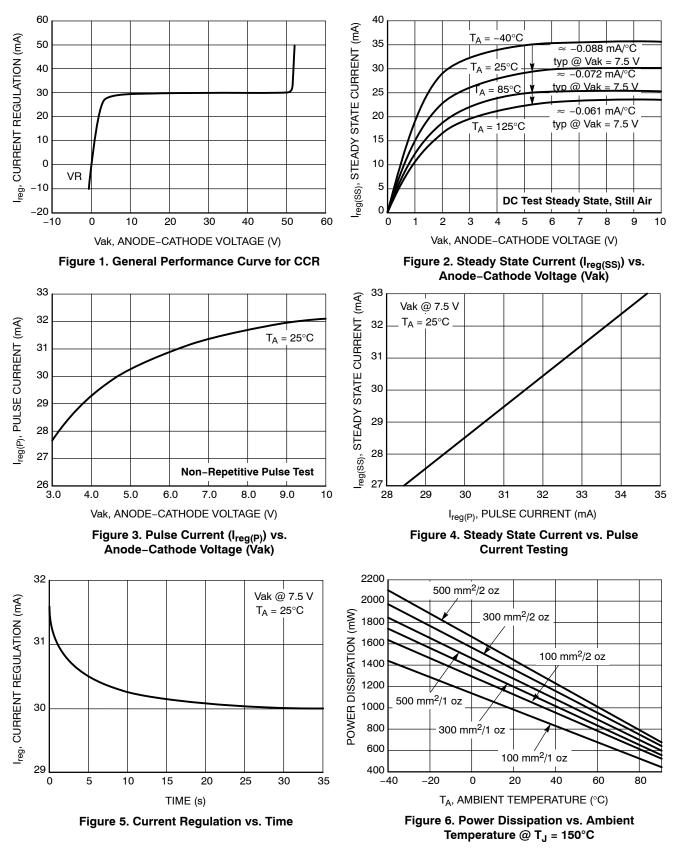
Characteristic Symbol		Max	Unit
Total Device Dissipation (Note 5) $T_A = 25^{\circ}C$ Derate above $25^{\circ}C$	PD	954 7.6	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 5)	$R_{ hetaJA}$	131	°C/W
Thermal Reference, Junction-to-Lead 4 (Note 5)	$R_{\psiJL4}$	40.8	°C/W
Total Device Dissipation (Note 6) $T_A = 25^{\circ}C$ Derate above 25°C	PD	1074 8.6	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 6)	R <sub>0JA</sub>	116	°C/W
Thermal Reference, Junction-to-Lead 4 (Note 6)	$R_{\psiJL4}$	39.9	°C/W
Total Device Dissipation (Note 7) $T_A = 25^{\circ}C$ Derate above $25^{\circ}C$	PD	1150 9.2	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 7)	$R_{ hetaJA}$	109	°C/W
Thermal Reference, Junction-to-Lead 4 (Note 7)	$R_{\psiJL4}$	42	°C/W
Total Device Dissipation (Note 8) $T_A = 25^{\circ}C$ Derate above $25^{\circ}C$	PD	1300 10.4	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 8)	$R_{ ext{ heta}JA}$	96	°C/W
Thermal Reference, Junction-to-Lead 4 (Note 8)	$R_{\psiJL4}$	39.4	°C/W
Total Device Dissipation (Note 9) $T_A = 25^{\circ}C$ Derate above $25^{\circ}C$	PD	1214 9.7	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 9)	$R_{ hetaJA}$	103	°C/W
Thermal Reference, Junction-to-Lead 4 (Note 9)	$R_{\psiJL4}$	40.2	°C/W
Total Device Dissipation (Note 10) $T_A = 25^{\circ}C$ Derate above 25°C	PD	1389 11.1	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 10)	R <sub>0JA</sub>	90	°C/W
Thermal Reference, Junction-to-Lead 4 (Note 10)	$R_{\psiJL4}$	37.7	°C/W
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

5. FR-4 @ 100 mm<sup>2</sup>, 1 oz. copper traces, still air. 6. FR-4 @ 100 mm<sup>2</sup>, 2 oz. copper traces, still air. 7. FR-4 @ 300 mm<sup>2</sup>, 1 oz. copper traces, still air. 8. FR-4 @ 300 mm<sup>2</sup>, 2 oz. copper traces, still air. 9. FR-4 @ 500 mm<sup>2</sup>, 1 oz. copper traces, still air. 10.FR-4 @ 500 mm<sup>2</sup>, 2 oz. copper traces, still air.

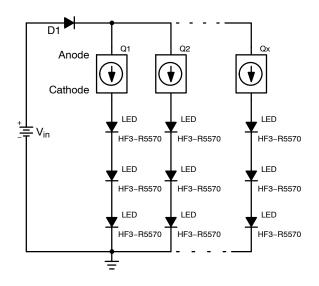
NOTE: Lead measurements are made by non-contact methods such as IR with treated surface to increase emissivity to 0.9. Lead temperature measurement by attaching a T/C may yield values as high as 30% higher °C/W values based upon empirical measurements and method of attachment.

# **TYPICAL PERFORMANCE CURVES**

Minimum FR-4 @ 300 mm<sup>2</sup>, 2 oz Copper Trace, Still Air

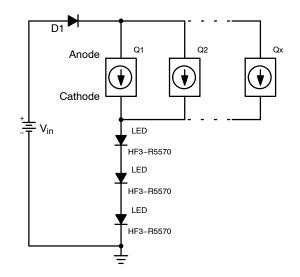


# APPLICATIONS



# Figure 7. Typical Application Circuit (30 mA each LED String)

Number of LED's that can be connected is determined by: D1 is a reverse battery protection diode LED's = ( $(V_{in} - Q_X V_F + D1 V_F)/LED V_F$ ) Example:  $V_{in} = 12 Vdc$ ,  $Q_X V_F = 3.5 Vdc$ , D1VF = 0.7 VLED  $V_F = 2.2 Vdc$  @ 30 mA (12 Vdc - 4.2 Vdc)/2.2 Vdc = 3 LEDs in series.

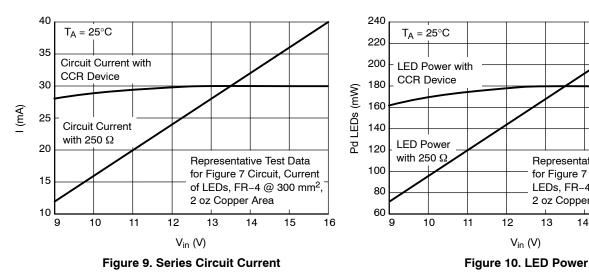


## Figure 8. Typical Application Circuit (90 mA each LED String)

Number of LED's that can be connected is determined by: D1 is a reverse battery protection diode Example:  $V_{in} = 12$  Vdc,  $Q_X V_F = 3.5$  Vdc, D1VF = 0.7 V LED  $V_F = 2.6$  Vdc @ 90 mA (12 Vdc - (3.5 + 0.7 Vdc))/2.6 Vdc = 3 LEDs in series. Number of Drivers = LED current/30 mA 90 mA/30 mA = 3 Drivers (Q1, Q2, Q3)

ON Semiconductor CCR Design	Resistor Biased Design	
Constant brightness over full Automotive Supply Voltage (more efficient), see Figure 9	Large variations in brightness over full Automotive Supply Voltage	
Little variation of power in LEDs, see Figure 10	Large variations of current (power) in LEDs	
Constant current extends LED strings lifetime, see Figure 9	High Supply Voltage/ Higher Current in LED strings limits lifetime	
Current decreases as voltage increases, see Figure 9	Current increases as voltage increases	
Current supplied to LED string decreases as temperature increases (self-limiting), see Figure 2	LED current decreases as temperature increases	
No resistors needed	Requires costly inventory (need for several resistor values to match LED intensity)	
Fewer components, less board space required	More components, more board space required	
Surface mount component	Through-hole components	







Steady-State (I<sub>reg(SS)</sub>)

There are two methods to measure current regulation: Pulse mode  $(I_{reg(P)})$  testing is applicable for factory and incoming inspection of a CCR where test times are a minimum. (t  $\leq$  300 µs). DC Steady-State (I<sub>reg(SS)</sub>) testing is applicable for application verification where the CCR will be operational for seconds, minutes, or even hours. ON Semiconductor has correlated the difference in  $I_{reg(P)}$  to

 $I_{reg(SS)}$  for stated board material, size, copper area and copper thickness.  $I_{reg(P)}$  will always be greater than  $I_{reg(SS)}$ due to the die temperature rising during Ireg(SS). This heating effect can be minimized during circuit design with the correct selection of board material, metal trace size and weight, for the operating current, voltage, board operating temperature (T<sub>A</sub>) and package. (Refer to Thermal Characteristics table).

Representative Test Data

for Figure 7 Circuit, Pd of

LEDs, FR-4 @ 300 mm<sup>2</sup>,

15

16

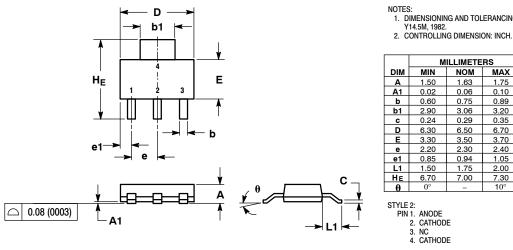
2 oz Copper Area

14

13

#### PACKAGE DIMENSIONS

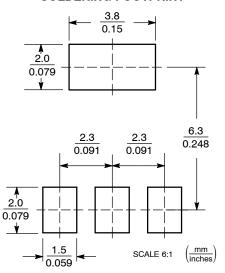
SOT-223 (TO-261) CASE 318E-04 **ISSUE M** 



1. DIMENSIONING AND TOLERANCING PER ANSI

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	1.50	1.63	1.75	0.060	0.064	0.068
A1	0.02	0.06	0.10	0.001	0.002	0.004
b	0.60	0.75	0.89	0.024	0.030	0.035
b1	2.90	3.06	3.20	0.115	0.121	0.126
С	0.24	0.29	0.35	0.009	0.012	0.014
D	6.30	6.50	6.70	0.249	0.256	0.263
E	3.30	3.50	3.70	0.130	0.138	0.145
е	2.20	2.30	2.40	0.087	0.091	0.094
e1	0.85	0.94	1.05	0.033	0.037	0.041
L1	1.50	1.75	2.00	0.060	0.069	0.078
HE	6.70	7.00	7.30	0.264	0.276	0.287
θ	0°	-	10°	0°	-	10°

SOLDERING FOOTPRINT



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