FL tube driver **BU2872AK**

The BU2872AK is a driver IC for the FL display unit. It has a total of 22 high withstanding voltage output pins, and supports displays ranging from 11-segment 11-character displays to 16-segment 4-character displays. A key scan function, LED drive output, and a general-purpose input pin make this IC ideal for use in the front panels of VTRs and other equipment. A serial interface enables control of all functions through a microcomputer.

Applications

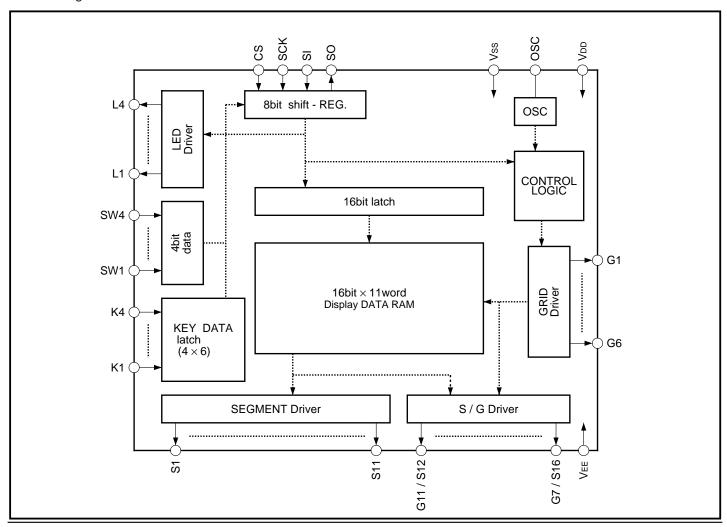
VCRs

Features

- 1) High withstanding voltage output.
- 2) Display modes: $11S \times 11G \sim 16S \times 4G$
- 3) Variable display luminance (7 steps).
- 4) 3-wire serial interface.

- 5) Key scanning function (6×4) .
- 6) Internal pull-down resistance (high withstanding voltage output).
- 7) QFP 44-pin package.

Block diagram



Standard ICs BU2872AK

Pin descriptions

Pin No.	Pin	Name	1/0	Function
14, 38	V _{DD}	Power supply pin 1	I	Connected to the power supply of the system
44	OSC	Oscillation pin	1/0	Connected to the oscillation capacitor
7, 43	Vss	GND pin	ı	Connected to the ground of the system
6	SI	Serial data input	ı	Serial data input starting from MSB
5	SO	Serial data output	0	Serial data output starting from MSB
8	SCK	Serial clock input	ı	Serial data read at rising edge
9	CS	Serial chip select	ı	Serial initialization at "L", effective at "H"
15 ~ 25	S1 ~ S11	High withstanding voltage output pin for segment	0	Output pin for segment Output: Pch open drain + pull-down resistance
27	VEE	Power supply pin 2	ı	Pull-down resistance connection for FLP driver
37 ~ 32	G1 ~ G6	High withstanding voltage output pin for grid	0	Output pin for grid Output: Pch open drain + pull-down resistance
26, 28 ~ 31	S12 / G11 ~ S16 / G7	High withstanding voltage output pin for segment / grid	0	Segment / grid output selectable pin Output: Pch open drain + pull-down resistance
42 ~ 39	L1 ~ L4	Output pin for LED	0	Output pin for LED; output is CMOS output
10 ~ 13	K1 ~ K4	Key data input pin	I	Data input pin for key scanning
1 ~ 4	SW1 ~ SW4	General-purpose input pin	I	General-purpose input pin Input data sent to microcomputer in serial format

●Absolute maximum ratings (Ta = 25°C, Vss = 0V)

Parameter	Symbol	Limits	Unit
Applied voltage 1	V _{DD}	− 0.3 ~ + 7.0	V
Applied voltage 2	VEE	VDD + 0.3 ~ VDD - 35	V
Input voltage	Vin	- 0.3 ~ V _{DD} + 0.3	V
Power dissipation	Pd	850*	mW
Operating temperature	Topr	– 25 ~ + 75	°C
Storage temperature	Tstg	− 55 ~ + 125	°C

Note) Operation is not guaranteed at these values.

Power dissipation is reduced by 8.5mW for each increase in Ta of 1°C over 25°C.

● Recommended operating conditions (Ta = 25°C, Vss = 0V)

Parameter	Symbol	Min.	Тур.	Max.	Unit
Operating power supply voltage 1	V _{DD}	4.5	5.0	5.5	V
Operating power supply voltage 2	VEE	V _{DD} – 32	V _{DD} – 30	V _{DD} – 0	V



^{*} When mounted on a $70\text{mm} \times 70\text{mm} \times 1.6\text{mm}$ glass epoxy board

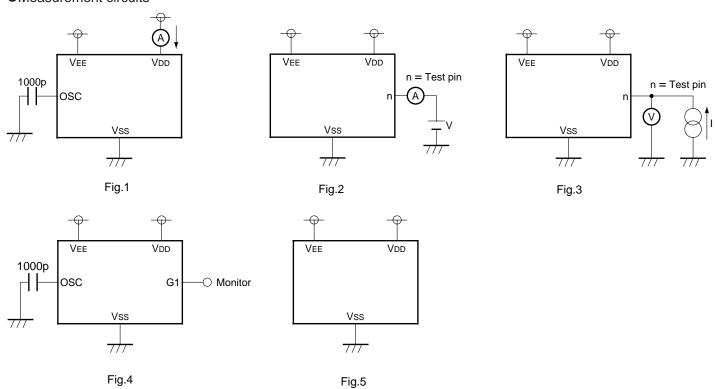
Standard ICs BU2872AK

●Electrical characteristics (unless otherwise noted, Ta = 25°C, Vdd = 5V, Vss = 0V, Vdd-Vee = 30V)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	Measurement circuit
Supply current	IDD	_	_	5	mA	Pin 44 external 1000 pF, when oscillated	Fig.1
Input threshold voltage	Vin	1.5	_	3.5	V	Pins 1 to 4, 6, 8, 9 to 13	Fig.5
Input current	lin	_	_	10	μΑ	Pins 1 to 4, 6, 8, 9 to 13	Fig.2
Oscillation frequency	Fosc	130	200	300	kHz	Pin 44 external 1000 pF	Fig.4
Segment output current	loseg	7	_	_	mA	Pins 15 to 26, 28 to 31 Vo = V _{DD} - 2V *2	Fig.2
Grid output current	lOgrd	20	_	_	mA	Pins 26, 28 to 37 Vo = V _{DD} – 2V *2	Fig.2
Off level leakage current	loff	_	_	10	μΑ	Pins 15 to 26, 28 to 37 Vo = VDD - VEE	Fig.2
Output pull-down resistance	R□	15	31	60	kΩ	Pins 15 to 26, 28 to 37	Fig.2
LED output high level voltage	Vон	4.0	_	_	V	Pins 39 to 42 lo = 1mA	Fig.3
LED output low level voltage	Vol		_	1.0	V	Pins 39 to 42 lo = - 10mA	Fig.3
⟨Serial transfer⟩							
Input data hold	Тѕн	0.16	_	_	μs	_	_
Input data setup	Tss	0.16	_	_	μs	_	_
Output data delay	To	_	_	0.3	μs	_	_
Input clock cycle	Tscyc	0.5		_	μs	_	_
Input clock "H" width	Т	40	_	60	%	Input clock cycle at minimum value	_

^{*2} Each of the high withstanding voltage output pins, pins 26 and 28 to 31, outputs the segment output current when set to the segment output and the grid current when set to the grid output.

•Measurement circuits



Standard ICs BU2872AK

Electrical characteristic curves

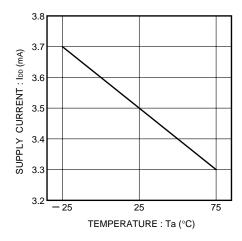


Fig.6 Supply current temperature characteristics

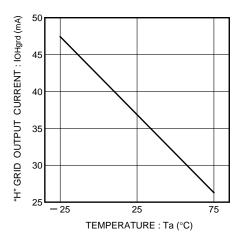


Fig.7 "H" grid output current temperature characteristics

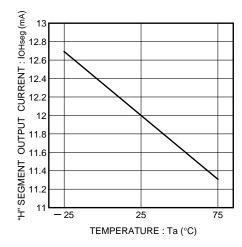


Fig.8 "H" segment output current temperature characteristics

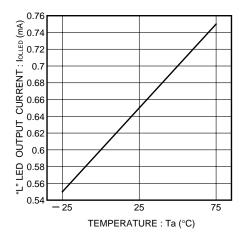
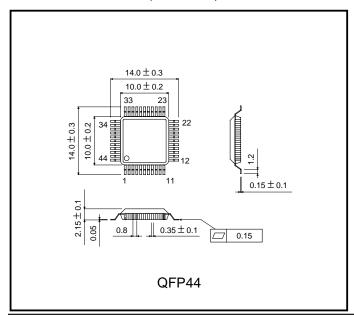


Fig. 9 "L" LED output current temperature characteristics

External dimensions (Units: mm)



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