

REV	DATE	DESCRIPTION
1	10/10/2010	REVISED FOR PRODUCTION
2	03/09/2011	BY TOSH ELECTRONICS COMPANY, ALL RIGHTS RESERVED

REV	DATE	DESCRIPTION	BY	CHK	APP
1	10/10/2010	ADDED PRELIMINARY	SHIBATA	LS	JJC
2	03/09/2011	REV PER ECO-06-003800	SHIBATA	BC	GG

- △ HOUSING: LCP, UL94V0, COLOR: BLACK.  
CONTACT: PHOSPHOR BRONZE.  
POST: BRASS WIRE
- △ CONTACT: 1.27µm MIN. TIN LEAD ON PCB TAIL  
OVER 1.27µm MIN. NICKEL OVER ALL.  
POST: 1.27µm MIN NICKEL PLATED.
- △ CONTACT: 1.27µm MIN. TIN ON PCB TAIL  
OVER 1.27µm MIN. NICKEL OVER ALL.  
POST: 1.27µm MIN NICKEL PLATED.
- △ MANUFACTURING TOLERANCE FOR:  
Ø0.46±0.05 FINISHED HOLE WITH Sn PB PLATING:  
DRILLED HOLE = Ø0.55±0.02  
COPPER PLATING = 0.025±0.050  
Sn PB PLATING = 0.0038-0.0124  
Ø0.47±0.05 FINISHED HOLE WITHOUT Sn PB PLATING:  
DRILLED HOLE = Ø0.55±0.02  
COPPER PLATING = 0.025±0.050
- △ SEE TABLE 1 FOR INTERCONNECTIONS TO  
BACKPLANE CONNECTOR.

TABLE 1  
INTERCONNECTIONS WITH BACKPLANE CONNECTOR 1410128

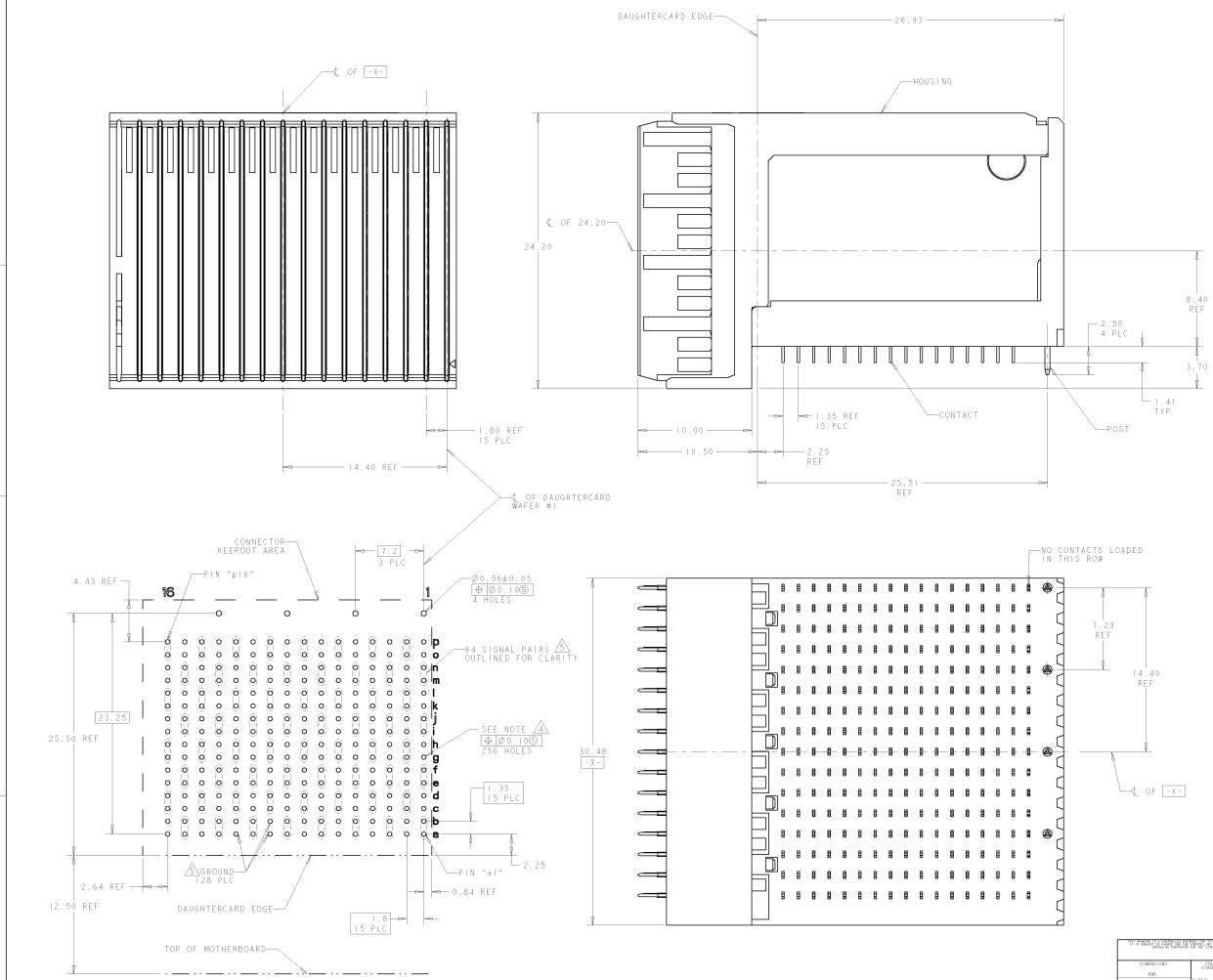
TYPICAL INTERCONNECTIONS FOR  
EACH ODD-NUMBERED  
COLUMN (WAFER): 1, 3, 5, 7, 9, 11, 13, 15

CONTACT USAGE	DAUGHTERCARD CONNECTOR PIN	BACKPLANE CONNECTOR PIN
SIGNAL PAIR	dx	dx
SIGNAL PAIR	dx	dx
SIGNAL PAIR	ex	ex
SIGNAL PAIR	fx	ex
SIGNAL PAIR	ix	ex
SIGNAL PAIR	jx	dx
SIGNAL PAIR	mx	ix
SIGNAL PAIR	nx	ix
GROUND	cx, dx, ex, fx, ix, jx, mx, nx, px (ALL COMMONED)	cx, fx, ix, jx (ALL COMMONED)

TYPICAL INTERCONNECTIONS FOR  
EACH EVEN-NUMBERED  
COLUMN (WAFER): 2, 4, 6, 8, 10, 12, 14, 16

CONTACT USAGE	DAUGHTERCARD CONNECTOR PIN	BACKPLANE CONNECTOR PIN
SIGNAL PAIR	cx	dx
SIGNAL PAIR	cx	cx
SIGNAL PAIR	dx	ex
SIGNAL PAIR	dx	fx
SIGNAL PAIR	ex	dx
SIGNAL PAIR	ix	ix
SIGNAL PAIR	ox	ix
SIGNAL PAIR	ox	ix
GROUND	cx, dx, ex, fx, ix, jx, mx, nx, ox (ALL COMMONED)	ox, dx, ox, jx (ALL COMMONED)

NOTE: "x" DESIGNATES THE COLUMN NUMBER.



PC BOARD LAYOUT  
(CONNECTOR SIDE)  
SCALE: 5:1

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