

DATE REVISIONS TO BE MAINTAINED	7	6	5	4	3	2	1
REVISIONS FOR FABRICATION	24						
ALL RIGHTS RESERVED							

REV	DATE	DESCRIPTION	BY	CHK	APP
B2		REVISED PER ECO-11-004817			

- HOUSING: LCP, UL94V0, COLOR: BLACK.
CONTACT: PHOSPHOR BRONZE.
POST: BRASS WIRE
- CONTACT: 1.27µm MIN GOLD IN PAD CONTACT AREA
1.27µm MIN TIN LEAD ON PCB TAIL
OVER 1.27µm MIN NICKEL OVER ALL.
POST: 1.27µm MIN NICKEL PLATED.
- MANUFACTURING TOLERANCE FOR Ø0.46±0.05
DIAMETER FINISHED HOLE WITH Sn/Pb PLATING:
DRILLED HOLE = Ø0.55±0.02
COPPER PLATING = 0.025-0.050
Sn/Pb PLATING = 0.0038-0.0124
- OR
- MANUFACTURING TOLERANCE FOR Ø0.475±0.05
DIAMETER FINISHED HOLE WITHOUT Sn/Pb PLATING:
DRILLED HOLE = Ø0.55±0.02
COPPER PLATING = 0.025-0.050
- SEE TABLE 1 FOR INTERCONNECTIONS TO
BACKPLANE CONNECTOR.

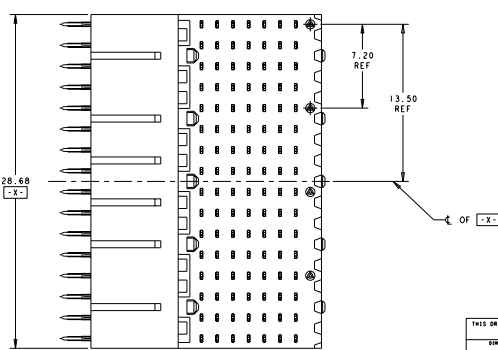
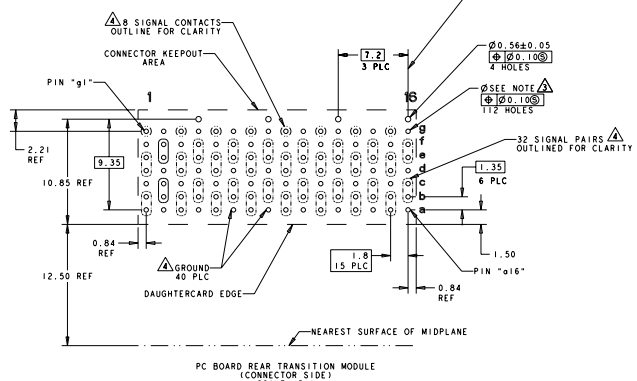
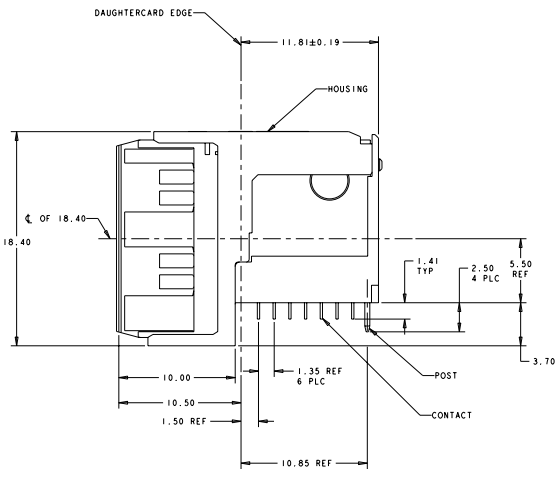
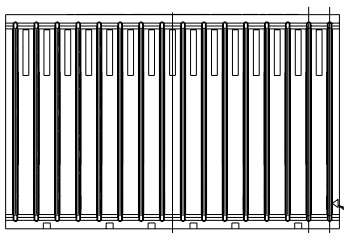


TABLE 1
INTERCONNECTIONS WITH BACKPLANE CONNECTOR
1410140-1, 1410142-1, 1410966-1, 1410967-1

TYPICAL INTERCONNECTIONS FOR EACH ODD-NUMBERED COLUMN (WAFERS: 1, 3, 5, 7, 9, 11, 13, 15)		
CONTACT USAGE	DAUGHTERCARD CONNECTOR PIN	BACKPLANE CONNECTOR PIN
SIGNAL PAIR	dx	dx
SIGNAL PAIR	bx	bx
SIGNAL PAIR	dx	ex
SIGNAL PAIR	ex	fx
SIGNAL PAIR	qx	ix
GROUND	cx, fx (ALL COMMONED)	cx, dx, gx, hx

TYPICAL INTERCONNECTIONS FOR EACH EVEN-NUMBERED COLUMN (WAFERS: 2, 4, 6, 8, 10, 12, 14, 16)		
CONTACT USAGE	DAUGHTERCARD CONNECTOR PIN	BACKPLANE CONNECTOR PIN
SIGNAL PAIR	bx	cx
SIGNAL PAIR	cx	dx
SIGNAL PAIR	dx	ex
SIGNAL PAIR	ex	fx
GROUND	qx, dx, gx, (ALL COMMONED)	dx, bx, ex, fx, ix

NOTE: "x" DESIGNATES THE COLUMN NUMBER

THIS DRAWING IS A CONTROLLED DOCUMENT. DATE: 22FEB2006. DRAWN BY: J. GILLESPIE. CHECKED BY: J. GILLESPIE. PART NO: 1410975-3. PLATING: 1410975-3. TE Connectivity. RIGHT-ANGLE PLUG ASSEMBLY, 7 ROW, CENTER, 20.5mm, Multi I/O, DIFFERENTIAL DAUGHTERCARD CONNECTOR, DIFFERENTIAL. DATE: 188-2012. SIZE: 114-13056. CUSTOMER DRAWING. PART NO: 1410975-3. SHEET: 6 OF 1. REV: B2.