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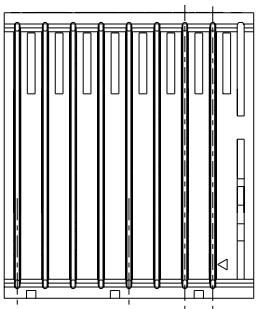
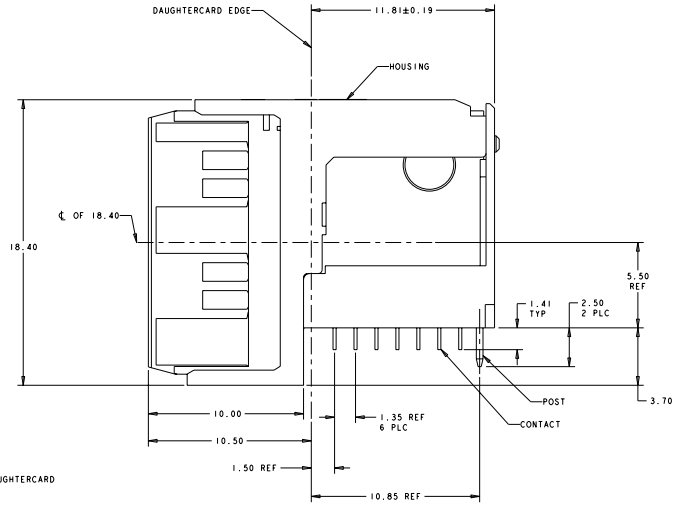
LOC	DIST
GP	00

REVISIONS

P	LTR	DESCRIPTION	DATE	DWN	APVD
	I	PROPOSED	21FEB2006	RT	GGG
	A	RELEASED	10DEC2006	RT	GG
	B	REV PER ECO-08-023229	11SEP2008	RT	GG



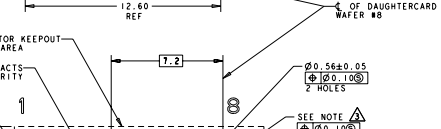


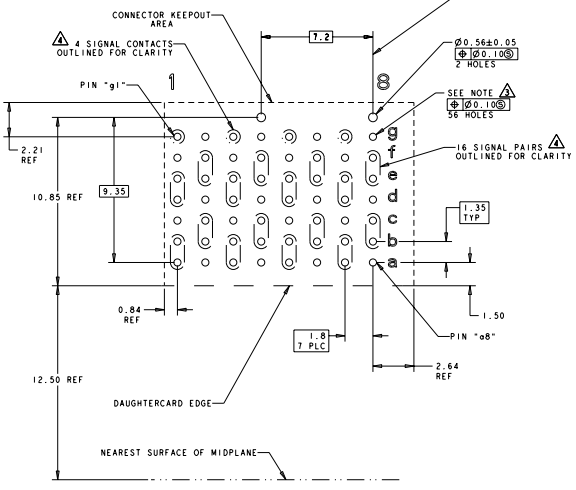
1. HOUSING: LCP, UL94V0; COLOR: BLACK.
   CONTACT: PHOSPHOR BRONZE.
   POST: BRASS WIRE
2. CONTACT: 1.27µm MIN GOLD IN PAD CONTACT AREA, 1.27µm MIN TIN-LEAD ON PCB TAIL OVER 1.27µm MIN. NICKEL OVER ALL.
   POST: 1.27µm MIN NICKEL PLATED.
3. MANUFACTURING TOLERANCE FOR Ø0.46± 0.05 DIAMETER FINISHED HOLE WITH SnPB PLATING:

   DRILLED HOLE = Ø0.55±0.02
   COPPER PLATING = 0.025-0.050
   Sn PB PLATING = 0.0038-0.0124

   OR MANUFACTURING TOLERANCE FOR Ø0.475± 0.05 DIAMETER FINISHED HOLE WITHOUT SnPB PLATING:

   DRILLED HOLE = Ø0.55±0.02
   COPPER PLATING = 0.025-0.050
4. SEE TABLE I FOR INTERCONNECTIONS TO BACKPLANE CONNECTOR.





PC BOARD REAR TRANSITION MODULE
(CONNECTOR SIDE)
SCALE 8:1

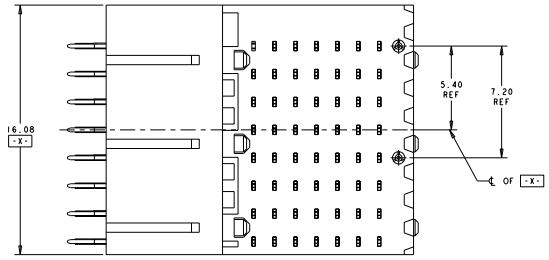


TABLE I
INTERCONNECTIONS WITH BACKPLANE CONNECTOR 1410186

TYPICAL INTERCONNECTIONS FOR ODD COLUMN (WAFER): 1,3,5,7		
CONTACT USAGE	DAUGHTERCARD CONNECTOR PIN	BACKPLANE CONNECTOR PIN
SIGNAL PAIR	ax	ax
	bx	bx
SIGNAL PAIR	dx	ex
	ex	fx
SIGNAL	gx	ix
GROUNDS	cx, fx (ALL COMMONED)	cx, dx, gx, hx

TYPICAL INTERCONNECTIONS FOR EVEN COLUMN (WAFER): 2,4,6,8		
CONTACT USAGE	DAUGHTERCARD CONNECTOR PIN	BACKPLANE CONNECTOR PIN
SIGNAL PAIR	bx	cx
	cx	dx
SIGNAL PAIR	ex	gx
	fx	hx
GROUNDS	ax, dx, gx, (ALL COMMONED)	ax, bx, ex, fx, ix

NOTE: "x" DESIGNATES THE COLUMN NUMBER

2	1410971-3
PLATING	PART NO

DRN	21FEB2006	Tyco Electronics Harrisburg, PA 17105-3608	
CHK D.TREA	21FEB2006		
CHK G.GRIFFITH	21FEB2006		
APVD	21FEB2006	NAME RIGHT-ANGLE PLUG ASSEMBLY, 7 ROW, HALF LEFT END, 20.3mm, Multig RT2, DAUGHTERCARD CONNECTOR, VITA 46	
PRODUCT SPEC 108-2072			
APPLICATION SPEC 114-13056		SIZE A1	CAGE CODE 00779
WEIGHT -		DRAWING NO C-1410971	RESTRICTED TO -
CUSTOMER DRAWING		SCALE 8:1	SHEET 1 OF 1
			REV B

DIMENSIONS: MM
TOLERANCES UNLESS OTHERWISE SPECIFIED:
6 PLC ±:
3 PLC ±0.5
2 PLC ±0.13
1 PLC ±:
ANGLES ±2°
MATERIAL -
FINISH SEE TABLE

AMP 4805 REV 31MAR2000
PRELIMINARY DRAWING

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