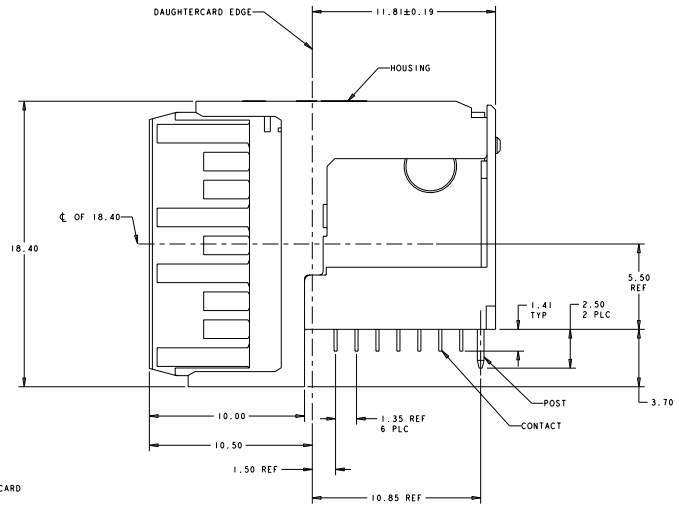
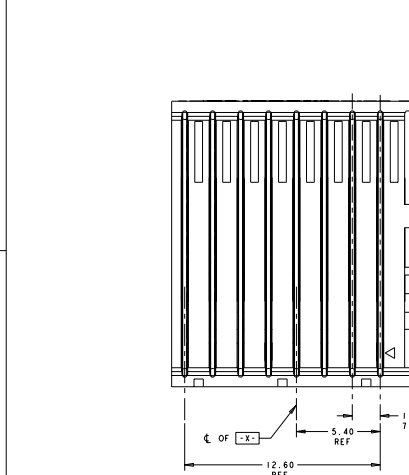


REV	DATE	DESCRIPTION	BY	CHK	APP
A	RELEASED			RT	GG
B	REV PER ECO-08-023229			RT	GG

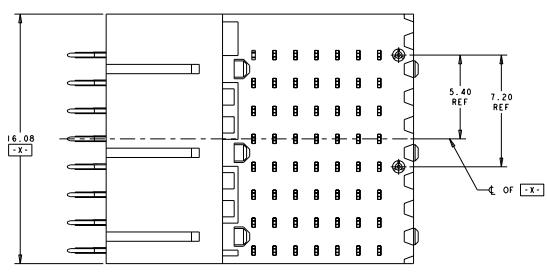
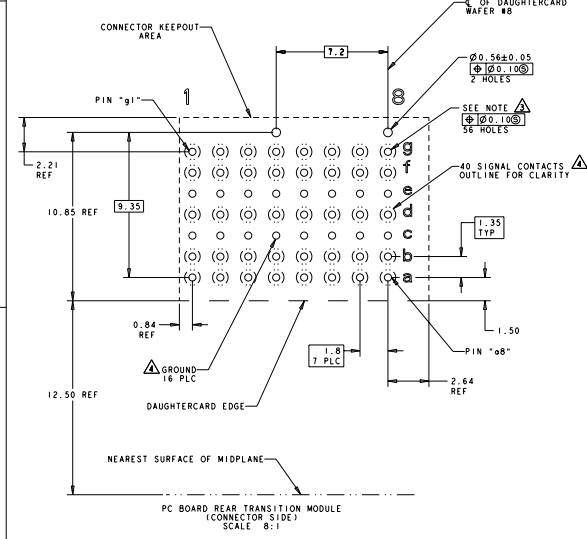


- HOUSING: LCP, UL94V0, COLOR: BLACK.  
CONTACT: PHOSPHOR BRONZE.  
POST: BRASS WIRE
- CONTACT: 1.27µm MIN GOLD IN PAD CONTACT AREA, 1.27µm MIN TIN-LEAD ON PCB TAIL OVER 1.27µm MIN. NICKEL OVER ALL.  
POST: 1.27µm MIN NICKEL PLATED.
- MANUFACTURING TOLERANCE FOR Ø.46±0.05 DIAMETER FINISHED HOLE WITH SnPB PLATING:  
DRILLED HOLE = Ø.55±0.02  
COPPER PLATING = 0.025-0.050  
Sn PB PLATING = 0.0038-0.0124  
OR MANUFACTURING TOLERANCE FOR Ø.475±0.05 DIAMETER FINISHED HOLE WITHOUT SnPB PLATING:  
DRILLED HOLE = Ø.55±0.02  
COPPER PLATING = 0.025-0.050
- SEE TABLE 1 FOR INTERCONNECTIONS TO BACKPLANE CONNECTOR.

TABLE 1  
INTERCONNECTIONS WITH BACKPLANE CONNECTOR 1410186-1

TYPICAL INTERCONNECTIONS FOR EACH COLUMN (WAFER): 1, 2, 3, 4, 5, 6, 7, 8		
CONTACT USAGE	DAUGHTERCARD CONNECTOR PIN	BACKPLANE CONNECTOR PIN
SIGNAL	ax	bx
SIGNAL	bx	cx
SIGNAL	dx	ex
SIGNAL	fx	gx
SIGNAL	gx	hx
GROUND	cx, ex, [ALL COMMONED]	ax, dx, fx, ix

NOTE: "\*" DESIGNATES THE COLUMN NUMBER



DATE: 08/20/2008		BY: PDC ELECTRONICS COMPANY	
REV: 00		DATE: 08/20/2008	
PART NO: 1410972-3		REV: 00	
PLATING		PART NO	
21FE82004		21FE82004	
21FE82004		21FE82004	
188-2812		188-2812	
114-13856		114-13856	
00778		00778	
1410972		1410972	
CUSTOMER DRAWING		CUSTOMER DRAWING	

RIGHT-ANGLE PLUG ASSEMBLY, 7 ROW, SE HALF LEFT END, 20.3mm, MultiSig RT2, DAUGHTERCARD CONNECTOR, VITA 46