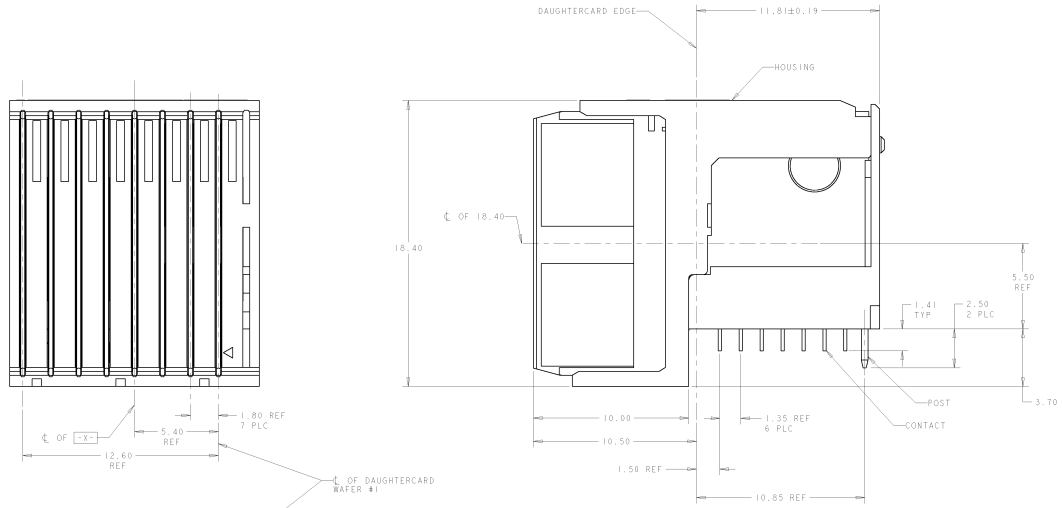


| REV | DATE | DESCRIPTION |
|-----|------------|-------------------------|
| 01 | 08/11/2010 | INITIAL RELEASE |
| 02 | 08/11/2010 | REVISED FOR FABRICATION |

| REV | DATE | DESCRIPTION | BY | CHKD | APPD |
|-----|------------|-------------------------|--------|------|------|
| 01 | 08/11/2010 | INITIAL RELEASE | 080808 | LS | JJC |
| 02 | 08/11/2010 | REVISED FOR FABRICATION | 080808 | LS | 06 |
| 03 | 08/11/2010 | ADDED - 3 AND NOTE 6 | 080808 | LS | 06 |
| 04 | 08/11/2010 | REV PER ECO-04-043300 | 080808 | BC | 06 |



HOUSING: LCP, UL94V0, COLOR: BLACK.
CONTACT: PHOSPHOR BRONZE.
POST: BRASS WIRE

CONTACT: 0.76µm MIN GOLD IN PAD CONTACT AREA, 1.27µm MIN TIN-LEAD ON PCB TAIL OVER 1.27µm MIN. NICKEL OVER ALL. POST: 1.27µm MIN NICKEL PLATED.

MANUFACTURING TOLERANCE FOR Ø, 4x±0.05 DIAMETER FINISHED HOLE WITH SAGB PLATING:
DRILLED HOLE = Ø0.55±0.02
COPPER PLATING = 0.025±0.050
OR
SAGB PLATING = 0.0038±0.0124
OR
MANUFACTURING TOLERANCE FOR Ø, 475±0.05 DIAMETER FINISHED HOLE WITHOUT SAGB PLATING:
DRILLED HOLE = Ø0.55±0.02
COPPER PLATING = 0.025±0.050

SEE TABLE 1 FOR INTERCONNECTIONS TO BACKPLANE CONNECTOR.

CONTACT: 0.76µm MIN GOLD IN PAD CONTACT AREA, 1.27µm MIN TIN-LEAD ON PCB TAIL OVER 1.27µm MIN. NICKEL OVER ALL. POST: 1.27µm MIN NICKEL PLATED.

CONTACT: 1.27µm MIN GOLD IN PAD CONTACT AREA, 1.27µm MIN TIN-LEAD ON PCB TAIL OVER 1.27µm MIN. NICKEL OVER ALL. POST: 1.27µm MIN NICKEL PLATED.

TABLE 1
INTERCONNECTIONS WITH BACKPLANE CONNECTOR 1410189

| TYPICAL INTERCONNECTIONS FOR COLUMN (WAFER): 8 | | |
|--|----------------------------|-------------------------|
| CONTACT USAGE | DAUGHTERCARD CONNECTOR PIN | BACKPLANE CONNECTOR PIN |
| SIGNAL PAIR | b _x | c _x |
| SIGNAL PAIR | e _x | g _x |
| GROUND | dx, dx, gx (ALL COMMONED) | ax, bx, ex, fx, ix |

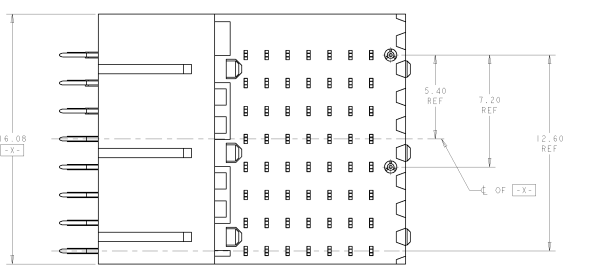
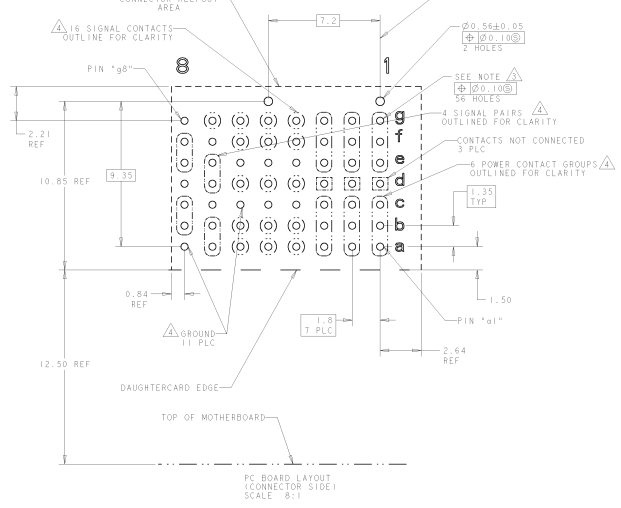
| TYPICAL INTERCONNECTIONS FOR COLUMN (WAFER): 7 | | |
|--|----------------------------|-------------------------|
| CONTACT USAGE | DAUGHTERCARD CONNECTOR PIN | BACKPLANE CONNECTOR PIN |
| SIGNAL PAIR | ax | gx |
| SIGNAL PAIR | b _x | bx |
| SIGNAL PAIR | dx | ex |
| SIGNAL PAIR | fx | fx |
| GROUND | ix, ix (ALL COMMONED) | cx, dx, gx, hx |

| TYPICAL INTERCONNECTIONS FOR EACH COLUMN (WAFER): 4, 5, 6 | | |
|---|----------------------------|-------------------------|
| CONTACT USAGE | DAUGHTERCARD CONNECTOR PIN | BACKPLANE CONNECTOR PIN |
| SIGNAL | ax | bx |
| SIGNAL | b _x | c _x |
| SIGNAL | dx | ex |
| SIGNAL | fx | gx |
| SIGNAL | gx | hx |
| GROUND | ix, ix (ALL COMMONED) | ax, dx, fx, ix |

| TYPICAL INTERCONNECTIONS FOR EACH COLUMN (WAFER): 1, 2, 3 | | |
|---|----------------------------|-------------------------|
| CONTACT USAGE | DAUGHTERCARD CONNECTOR PIN | BACKPLANE CONNECTOR PIN |
| POWER | ax, bx, cx | dx, bx, cx, dx |
| POWER | ex, fx, gx | fx, gx, hx, ix |
| NOT CONNECTED | dx | ex |

NOTE: "x" DESIGNATES THE COLUMN NUMBER

| | |
|---|-----------------|
| △ | 1410189-3 |
| △ | 1410189-2 |
| △ | 1410189-1 |
| △ | PLATING PART NO |



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