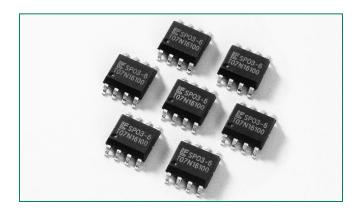
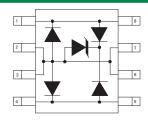
# HF RoHS PO GREEN SP03-6 (SO-8) Series



#### **Agency Approvals - Pending**

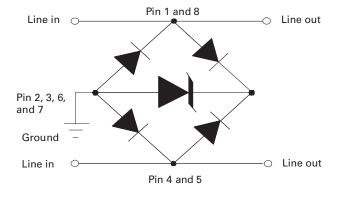
Agency	Agency File Number
<b>IR</b> <sub>®</sub>	E128662

#### **Pinout**



SO-8 (Top View)

#### **Functional Block Diagram**



### Description

This new broadband protection device from Littelfuse provides overvoltage protection for applications such as 10/100/1000 BaseT Ethernet, T3/E3 DS3 interfaces, ADSL2+, and VDSL2+. This new protector combines the TVS diode element with a diode rectifier bridge to provide both longitudinal and differential protection in one package. This design innovation results in a capacitive loading characteristic that is log-linear with respect to the signal voltage across the device. This reduces intermodulation (IM) distortion caused by a typical solid-state protection solution. The application schematic provides the connection information.

#### **Features**

- RoHS compliant
- MS-012 surface mount package (JEDEC SO-8)
- Low insertion loss, loglinear capacitance
- Combined longitudinal and metallic protection
- Clamping speed of nanoseconds
- UL 94V-0 epoxy molding
- Pending UL recognized component
- Low clamping voltage

#### **Applications**

- T1/E1 Line cards
- T3/E3 and DS3 Interfaces
- STS-1 Interfaces
- 10/100/1000 BaseT Ethernet

Life Support Note:

#### Not Intended for Use in Life Support or Life Saving Applications

The products shown herein are not designed for use in life sustaining or life saving applications unless otherwise expressly indicated.

©2010 Littelfuse, Inc. 159 SP03-6 (SO-8) Series

# SPA™ Silicon Protection Array Products Low Capacitance TVS protection for high-speed data interfaces



## **Absolute Maximum Ratings**

Parameter	Rating	Units
Peak Pulse Current (8/20µs)	150	А
Peak Pulse Power (8/20µs)	2800	W
IEC 61000-4-2, Direct Discharge, (Level 4)	30	kV
IEC 61000-4-2, Air Discharge, (Level 4)	30	kV
IEC 61000-4-5 (8/20μs)	100	А
Telcordia GR 1089 (Intra-Building) (2/10µs)	100	А
ITU K.20 (5/310μs)	40	A

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

#### **Thermal Information**

Parameter	Rating	Units
SOIC Package	170	°C/W
Operating Temperature Range	-55 to 125	°C
Storage Temperature Range	-65 to 150	°C
Maximum Junction Temperature	150	°C
Maximum Lead Temperature (Soldering 10s) (SOIC - Lead Tips Only)	260	°C

# Electrical Characteristics ( $T_{OP} = 25$ °C)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Units
Reverse Stand-Off Voltage	V <sub>RWM</sub>	-	-	-	6	V
Reverse Breakdown Voltage	V <sub>BR</sub>	I <sub>T</sub> = 1mA	6.8	-	-	V
Reverse Leakage Current	I <sub>R</sub>	V <sub>RWM</sub> = 6V, T= 25°C	-	-	25	μΑ
Clamping Voltage, Line-Ground	V <sub>c</sub>	I <sub>PP</sub> = 50A, t <sub>p</sub> =8/20 μs	-	-	15	V
Clamping Voltage, Line-Ground	V <sub>c</sub>	$I_{pp}$ = 100A, $t_p$ =8/20 µs	-	-	20	V
Junation Consoitance	C <sub>j</sub> (Line-Ground)	Between I/O Pins and Ground V <sub>B</sub> =0V, f= 1MHz	-	16	25	pF
Junction Capacitance	C <sub>j</sub> (Line-Line)	Between I/O Pins V <sub>p</sub> =0V, f= 1MHz	-	8	12	pF



Figure 1: Non-repetitive Peak Pulse Current vs. Pulse Time



**Figure 2: Current Derating Curve** 

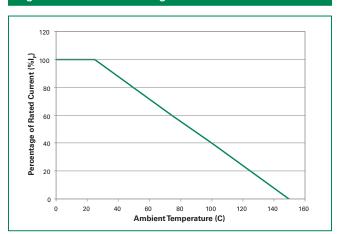


Figure 3: Pulse Waveform

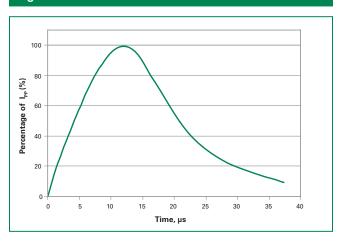


Figure 4: Clamping Voltage vs. Peak Pulse Current

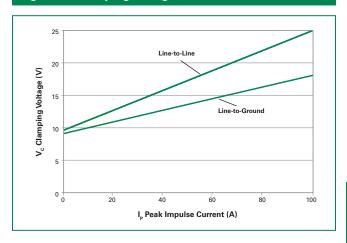


Figure 5: Capacitance vs. Reverse Voltage

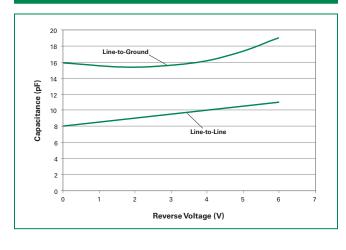
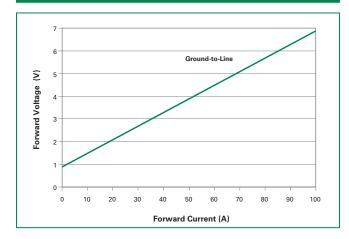


Figure 6: Forward Voltage vs. Forward Current



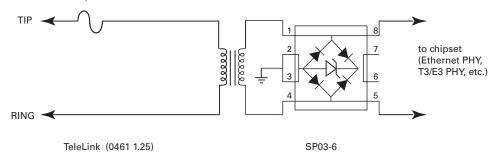
# SPA™ Silicon Protection Array Products Low Capacitance TVS protection for high-speed data interfaces



#### **Application Example**

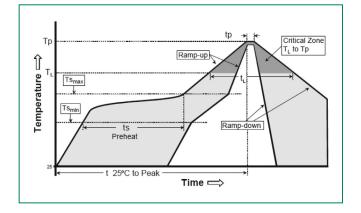
The following schematic shows a high-speed data interface protection solution. The SP03-6 provides both metallic (differential) and longitudinal (common mode) protection from lightning induced surge events. Its surge rating is compatible with the intra-building surge requirements of Telcordia's GR-1089-CORE, and the Basic Level

Recommendations of ITU K.20 and .21. This device protects against both positive and negative induced surge events. The TeleLink fuse provides overcurrent protection for the long term 50/60 Hz power fault events.



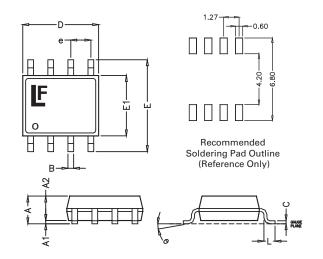
# **Soldering Parameters**

Reflow Condition		Pb – Free assembly	
	-Temperature Min (T <sub>s(min)</sub> )	150°C	
Pre Heat	-Temperature Max (T <sub>s(max)</sub> )	200°C	
	-Time (min to max) (t <sub>s</sub> )	60 – 180 secs	
Average ramp up rate (Liquidus) Temp (T <sub>1</sub> ) to peak		3°C/second max	
T <sub>S(max)</sub> to T <sub>L</sub> - Ramp-up Rate		3°C/second max	
Reflow	-Temperature (T <sub>L</sub> ) (Liquidus)	217°C	
Reflow	-Temperature (t <sub>L</sub> )	60 – 150 seconds	
PeakTemperature (T <sub>p</sub> )		250+ <sup>0/-5</sup> °C	
Time within 5°C of actual peak Temperature (tp)		20 - 40 seconds	
Ramp-down Rate		6°C/second max	
Time 25°C to peakTemperature (T <sub>P</sub> )		8 minutes Max.	
Do not exceed		260°C	



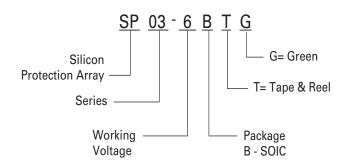


#### Package Dimensions - Mechanical Drawings and Recommended Solder Pad Outline

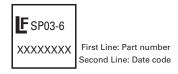


Package	MS-012 (SO-8)						
Pins	8						
JEDEC	MO-223 Issue A						
	Millimetres Inches				Millimetres		hes
	Min	Max	Min Max				
Α	1.35	1.75	0.053	0.069			
A1	0.10	0.25	0.004	0.010			
A2	1.25	1.65	0.049	0.065			
В	0.31	0.51	0.012	0.020			
С	0.17	0.25	0.007	0.010			
D	4.80	5.00	0.189	0.197			
E	5.80	6.20	0.228	0.244			
E1	3.80	4.00	0.150	0.157			
е	1.27 BSC 0.050 BSC						
L	0.40 1.27 0.016 0.050						

#### **Part Numbering System**



### **Part Marking System**



#### **Product Characteristics**

Lead Plating	Matte Tin
Lead Material	Copper Alloy
Lead Coplanarity	0.004 inches (0.102mm)
Subsitute Material	Silicon
Body Material	Molded Epoxy
Flammability	UL94-V-0

#### Notes:

- 1. All dimensions are in millimeters
- 2. Dimensions include solder plating.
- 3. Dimensions are exclusive of mold flash & metal burr.
- 4. All specifications comply to JEDEC SPEC MO-223 Issue A
- 5. Blo is facing up for mold and facing down for trim/form, i.e. reverse trim/form.
- 6. Package surface matte finish VDI 11-13.

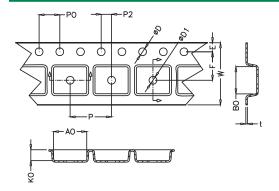
# **Ordering Information**

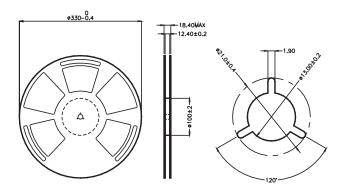
Part Number	Package	Marking	Min. Order Qty.
SP03-6BTG	SOIC Tape & Reel	SP03-6	2500

# SPA™ Silicon Protection Array Products Low Capacitance TVS protection for high-speed data interfaces



# **Embossed Carrier Tape & Reel Specification - SOIC Package**





# **Dimensions**

	Millimetres		Inches		
	Min	Max	Min	Max	
E	1.65	1.85	0.065	0.073	
F	5.4	5.6	0.213	0.22	
P2	1.95	2.05	0.077	0.081	
D	1.5	1.6	0.059	0.063	
D1	1.50 Min		0.059 Min		
P0	3.9	4.1	0.154	0.161	
10P0	40.0 +/- 0.20		0 1.574 +/- 0.008		
W	11.9	12.1	0.468	0.476	
P	7.9	8.1	0.311	0.319	
A0	6.3	6.5	0.248	0.256	
В0	5.1	5.3	0.2	0.209	
K0	2	2.2	0.079	0.087	
t	0.30 +	/- 0.05	0.012 +/- 0.002		