

SMTYxxA

Low forward voltage TVS: Transky™

Main applications

 Power rail ESD transient over-voltages and reverse voltages protection for 5 and 12 V supplied IC's

Description

The Transky is designed specifically for miniaturized electronic devices and equipment subject to ESD transient over-voltages. The Transky combines the performance of a Transil™ or TVS (Transient Voltage Suppressor) and low forward voltage Schottky diode in a monolithic structure.

It offers both an overshoot protection in the 6.4 V or 13.2 V clamping ranges and a negative spike protection in the -0.48 V clamping range compared to the -1 V with the standard Transil family on the 5 or 12 V power line.

Its 600 W power capability offers high transient capability with SMA package.

Features

- Integration of a Transil with a Schottky diode
- JEDEC registred SMA package outline
- Low clamping factor V_{CI} / V_{BI}?
- Fast response time
- RoHS compliant

Benefit :

- Optimized PCB area: up to 62% space saving versus discrete solution
- High peak pulse power: up to 600 W
- Stand-off voltage: 5 V for SMTY5.0A 12 V for SMTY12A
- Low forward voltage: 0.48 V @ 1 A
- Very low leakage current:
 10 μA @ 5 V for SMTY5.0A
 20 μA @ 12 V for SMTY12A

Order code

Part number	Marking
SMTY5.0A	Y5.0
SMTY12A	Y12

Complies with following standard

IEC 61000-4-2 Level 4

Air discharge 15 kV Contact discharge 8 kV

April 2006

Rev 1

Characteristics 1

Table 1.	Absolute ratings (limitin	ig value)
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Symbol	Parameter		Value	Unit		
V _{pp}	IEC 61000-4-2 level 4 standard	Air discharge Contact discharge	15 8	kV		
Р	Power dissipation on infinite heatsink	wer dissipation on infinite heatsink $T_{amb} = 25^{\circ} C$				
P _{PP}	Peak pulse Power dissipation ⁽¹⁾	600	W			
I _{FSM}	Non repetitive surge peak forward current	40	A			
T _{stg}	-65 to +175	°C				
Тj	Maximum operating junction temperature ⁽²⁾ 150 °C					
 10/1000µs pulse waveform dPtot/dTj < 1/Rth(j-a) thermal runaway condition for a Transky Table 2. Thermal resistance 						
Symbo	Parameter	~0	Value	Unit		

Table 2. Thermal resistance

Symbol	Parameter	01	Value	Unit
R _{th(j-a)}	Junction to ambient on printed circuit	ש`	120	°C/W
R _{th(j-l)}	Junction to lead	101	30	°C/W

Electrical characteristics Table 3.

Symbol	Parameter	
I _{RM}	Leakage current @ V _{RM}	I _F
V _{RM}	Stand-off voltage	
V_{BR}	Breakdown voltage	
I _R	Reverse leakage current	$ \begin{array}{c} \overset{\forall cL \forall BR}{\rightarrow} \overset{\forall rM}{\rightarrow} \\ \hline \\ \\ \hline \\ \hline \\ \\ \hline \\ \\ \hline \\ \\ \\ \hline \\ \\ \\ \\ \hline \\$
V _{CL}	Clamping voltage	I _R
I _{PP}	Peak pulse current	I _{PP}
V _F	Forward voltage drop	

10	V _F F	orward vo	ltage dr	ор				ſ	-		
00501		I _{RM max} (@ V _{RM}	I _{RM max} V _{RM} @ 8	x @ 85° C	V _{BR mi}	_{in} @ I _R	V _{CL} max 10/10	α @ I _{PP})0 μs	V _F max @ 1A ⁽¹⁾	α T max
U		μΑ	v	mA	v	v	mA	v	Α	V	10 ⁻⁴ /°C
	SMTY5.0A	10	5	0.5	5	6.4	10	9	43.5	0.48	10
	SMTY12A	20	12	1.2	12	13.2	1	18.5	31	0.48	10
	1. Pulse test	t _p = 500 μs,	δ < 2%								

10.00

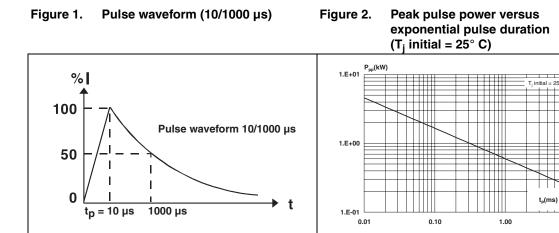
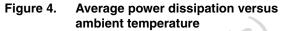
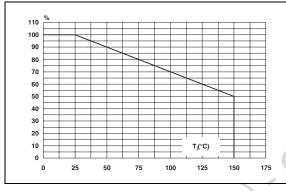
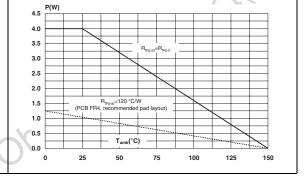


Figure 3. Relative variation of peak pulse power versus initial junction temperature







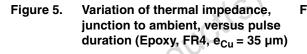
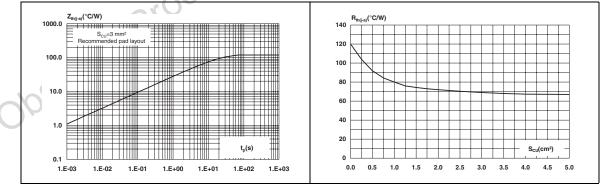
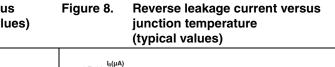


Figure 6. Thermal resistance, junction to ambient, versus copper surface under each lead (printed circuit board FR4, e_{Cu} = 35 μm)



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Figure 7. Forward voltage drop versus forward current (typical values)



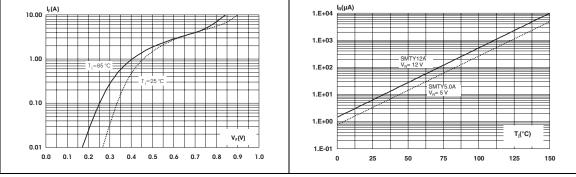
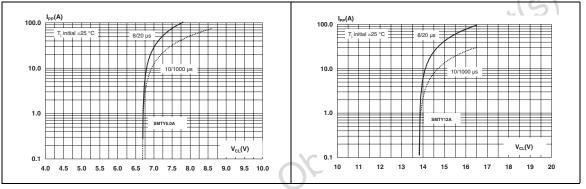
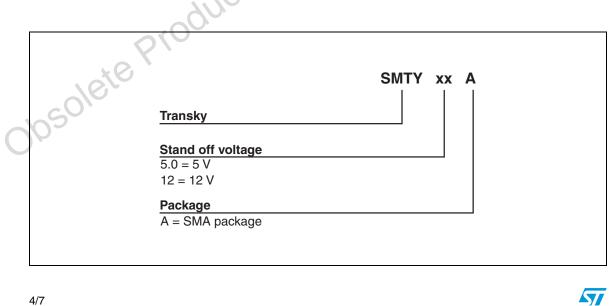


Figure 9. SMTY5.0A Clamping voltage versus Figure 10. SMTY12A Clamping voltage versus peak pulse current (typical values)





Ordering information scheme 2

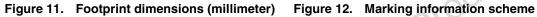


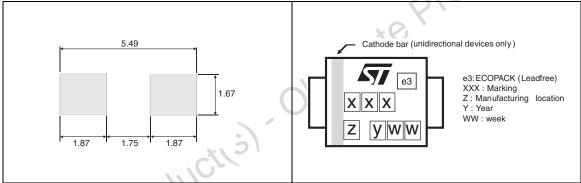
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3 Package information

			Dimer	nsions	
E1	Ref.	Millin	neters	Inc	hes
		Min.	Max.	Min.	Max.
D	A1	1.90	2.03	0.075	0.080
	A2	0.05	0.20	0.002	0.008
_	b	1.25	1.65	0.049	0.065
	с	0.15	0.41	0.006	0.016
	E	4.80	5.60	0.189	0.220
	E1	3.95	4.60	0.156	0.181
	D	2.25	2.95	0.089	0.116
	L	0.75	1.60	0.030	0.063

Table 4. SMA (plastic) dimensions





In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.



4 Ordering information

Ordering type	Marking	Package	Weight	Base quantity	Delivery mode
SMTY5.0A	Y5.0	SMA	0.068g	5000	Tape and Reel
SMTY12A	Y12	SMA	0.068g	5000	Tape and Reel

5 Revision history

24-Apr-2006 1 Initial release.	Date	Revision	Changes
obsolete Productls	24-Apr-2006	1	Initial release.
solete Product(s)	24-Apr-2006	1 ductl	Initial release.

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